



NAGARJUNA

COLLEGE OF ENGINEERING & TECHNOLOGY

***Outcome Based Education(OBE)/
Choice Based Credit System (CBCS) Curricula***

2019-2020

Department of Electronics & Communication Engineering

NAGARJUNA COLLEGE OF ENGINEERING & TECHNOLOGY

Mudugurki Village, VenkatagiriKote Post,Devanahalli taluk,
Bangalore district - 562 164

PRINCIPAL

Nagarjuna College of Engineering & Technology
Devanahalli (Tq) Bengaluru (Dt.)-Pin: 562164



An Autonomous College under VTU

DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING

VISION

To transform the students as leaders in Electronics & Communication Engineering to achieve professional excellence in the challenging future

MISSION

- M1: To create an environment for the students to have strong academic fundamentals and enable them to be life-long learners.
- M2: To provide modern tools to the students in the field of electronics and communication to meet the real-world challenges.
- M3: To develop Communication skill, leadership qualities, team work and skills for continuing education among the students.
- M4: To inculcate Ethics, Human values and skills for solving societal problems and environmental protection.
- M5: Validate engineering knowledge through innovative research projects to enhance their employability and entrepreneurship skills.

III to VIII Semesters

Scheme and Syllabus

With effect from Academic Year 2019-20

Program Educational Objectives (PEOs)

PEO-1: Graduates of Electronics and Communication engineering will be using the basic academic knowledge of design and analysis required in the industry for sustainable societal growth.

PEO-2: Graduates of Electronics and Communication engineering will be able to design project based learning and team based learning.

PEO-3: Graduates in Electronics and Communication engineering will demonstrate good communication skills, dynamic leadership qualities with concern for environmental protection.

PEO-4: Electronics and Communication engineering graduates will be capable of pursuing higher studies, take up research and development work blended with ethics and human values.

PEO-5: Electronics and Communication engineering graduates will have the ability to get employed and become entrepreneurs thereby switching over from responsive engineering to creative engineering.

Program Outcomes and Program Specific Outcomes as defined by the Program

Program Outcome:

PO1: Engineering Knowledge: Apply the knowledge of mathematics, science, engineering fundamentals, and electronics and communication engineering principles to the solution of complex problems in electronics and communication engineering.

PO2: Problem Analysis: Identify, formulate, research literature, and analyze complex electronics and communication engineering problems reaching substantiated conclusions using first principles of mathematics, and engineering sciences.

PO3: Design/Development of Solutions: Design solutions for complex electronics and communication engineering problems and design system components or processes that meet the specified needs with appropriate consideration for the public health and safety, and the cultural, societal, and environmental considerations.

PO4: Conduct Investigations of Complex problems: Use research-based knowledge and research methods including design of experiments, analysis and interpretation of data, and synthesis of the information to provide valid conclusions related to electronics and communication engineering problems.

PO5: Modern Tool Usage: Create, select, and apply appropriate techniques, resources, and modern engineering and IT tools including prediction and modelling to complex electronics and communication engineering activities with an understanding of the limitations.

PO6: The Engineer and Society: Apply reasoning informed by the contextual knowledge to assess societal, health, safety, legal and cultural issues and the consequent responsibilities

relevant to the professional electronics and communication engineering practice.

PO7: Environment and Sustainability: Understand the impact of the professional electronics and communication engineering solutions in societal and environmental contexts, and demonstrate the knowledge of, and need for sustainable development.

PO8: Ethics: Apply ethical principles and commit to professional ethics and responsibilities and norms of the electronics and communication engineering practice.

PO9: Individual and Teamwork: Function effectively as an individual, and as a member or leader in diverse teams, and in multidisciplinary settings.

PO10: Communication: Communicate effectively on complex electronics and communication engineering activities with the engineering community and with society at large, such as, being able to comprehend and write effective reports and design documentation, make effective presentations, and give and receive clear instructions.

PO11: Project management and finance: Demonstrate knowledge and understanding of the engineering and management principles and apply these to one's own work, as a member and leader in a team, to manage projects and in multidisciplinary environments.

PO12: Life Long Learning: Recognize the need for, and have the preparation and ability to engage in independent and life-long learning in the broadest context of technological change.

Program Specific Outcome (PSO):

PSO1. Graduate will be able to identify, analyze & solve the problems related to Electronics and Communication Engineering by applying the fundamental knowledge of Electronics and Communication.

PSO2. Graduate will demonstrate an ability to investigate, design and develop both software and hardware using significant knowledge of modern tools in Electronics and Communication Engineering.

PSO3. Graduate will be able to apply their knowledge to assess societal, environmental, health, safety issues with professional ethics and can also pursue higher studies, involve in research activities, be employable or entrepreneur.

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Third Semester B.E.-Scheme

Sl. No.	Course Code	Course	Teaching Dept.	L:T:P:S (Hrs/week)	Total Credits	Marks
1	18ECM31	Integral Transforms with SCILAB (IC)	Maths	3:0:2:0	4	100
2	18ECT32	Analog Electronic Circuits	EC	4:0:0:0	4	100
3	18ECI33	Digital Electronic Circuits(IC)	EC	3:0:2:0	4	100
4	18ECI34	Network Analysis (IC)	EC	3:0:2:0	4	100
5	18ECT35X	Foundation Elective – I	EC	4:0:0:0	4	100
6	18ECL36	Analog Electronic Circuits Laboratory	EC	1:0:2:0	2	100
7	18CSD37	Career Skill Development Programme	S&H	1:2:0:0	2	100
8	18CPH38	Constitution of India and Professional Ethics and Human Rights	S&H	1:0:0:0	1	100
TOTAL				20-2-8-0	25	800

Foundation Elective – I (IC)

Sl. No.	Course Code	Course
1	18ECT351	Engineering Electromagnetics
2	18ECT352	Data Structures with C
3	18ECT353	Electronic Devices

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Fourth Semester B.E.-Scheme

Sl. No.	Course Code	Course	Teaching Dept	L:T:P:S (Hrs/week)	Total Credits	Marks
1	18ECM41	Fourier Series and Probability Theory (IC)	Maths	3:0:2:0	4	100
2	18ECT42	Fundamentals of HDL	EC	4:0:0:0	4	100
3	18ECI43	Signals and Systems (IC)	EC	3:0:2:0	4	100
4	18ECI44X	Foundation Elective – II (IC)	EC	3:0:2:0	4	100
5	18EET45X	Engineering Elective – III	EC	4:0:0:0	4	100
6	18ECL46	Fundamentals of HDL Lab	EC	1:0:2:0	2	100
7	18CSD47	Technical Report Writing & IRDP	S&H	1:2:0:0	2	100
8	18KAK38 / 18KAK38	Vyavaharika Kannada / Adalitha Kannada	S&H	1:0:0:0	1	100
TOTAL				20-2-8-0	25	800

Foundation Elective - II (IC)

SI. No.	Course Code	Course
1	18ECI441	Linear Integrated Circuits & Applications
2	18ECI442	Transmission Lines and Wave Guides (TLWG)
3	18ECI443	Electronic Instrumentation

Engineering Elective - III

SI. No.	Course Code	Course
1	18EET451	Renewable Energy Resources
2	18EET452	Introduction to Cyber Security and Cyber Laws
3	18EET453	Management Information System
4	18EET454	Environmental Air Pollution

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Fifth Semester B.E.-Scheme

Sl. No.	Course Code	Course	TeachingDept.	L-T-P-S (Hrs/week)	Total Credits	Marks
1	17ECT51	Communication Systems	EC	3-0-0-0	3	100
2	17ECT52	Microcontrollers	EC	3-0-0-0	3	100
3	17ECT53	Information Theory and Coding	EC	3-0-0-0	3	100
4	17ECI54X	Foundation Elective-IV (IC)	EC	3-0-2-0	4	100
5	17ECI55X	Foundation Elective-V (IC)	EC	3-0-2-0	4	100
6	17ECT56X	Engineering Elective-VI / PBL	EC/ME	3-0-0-0	3	100
7	17ECL57	Microcontroller Lab	EC	1-0-2-0	2	100
8	17ECL58	Communication System Lab	EC	1-0-2-0	2	100
9	17ECH59	General Aptitude	EC/BS&H	2-0-0-0	2	100
Total				22-0-8-0	26	900

Foundation Elective-IV (IC)

SI. No.	Course Code	Course
1	17ECI541	Control Systems
2	17ECI542	Low power VLSI Design
3	17ECI543	Microwave & Radar

Foundation Elective-V (IC)

SI. No.	Course Code	Course
1	17ECI551	Digital System Design using Verilog
2	17ECI552	Object Oriented Programming with JAVA
3	17ECI553	Online Certification course from IITs / IISc / SWAYAM / EDX

Engineering Elective-VI / PBL

SI. No.	Course Code	Course
1	17ECT561	Mechatronics
2	17ECT562	Energy Engineering and Management
3	17ECT563	Linear Algebra
4	17ECT564	Management Information Systems

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Sixth Semester B.E.-Scheme

Sl. No	Course Code	Course	TeachingDept.	L-T-P-S (Hrs/week)	Total Credits	Marks
1	17ECT61	Digital Signal Processing	EC	4-0-0-0	4	100
2	17ECT62	Digital Communication	EC	4-0-0-0	4	100
3	17ECI63X	Foundation Elective-VII (IC)	EC	3-0-2-0	4	100
4	17ECT64X	Engineering Elective-VIII/PBL	EC	3-0-0-0	3	100
5	17ECL65	Digital Signal Processing Lab	EC	1-0-2-0	2	100
6	17HOE66X	Open Elective-IX	EC/BS&H	2-0-0-4	3	100
7	17ECL67	Digital Communication Lab	EC	1-0-2-0	2	100
8	17ECH68	Technical aptitude and GD	EC/BS&H	2-0-0-0	2	100
9	17ECP69	Mini Project and Seminar	EC	0-0-4-0	2	100
Total				20-0-10-4	26	900

Foundation Elective-VII (IC)

Sl. No.	Course Code	Course
1	17ECI631	Antenna and Propagation
2	17ECI632	Database Concepts
3	17ECI633	Online certification courses from IITs / IISC / SWAYAM / EDX

Engineering Elective-VIII / PBL

Sl. No.	Course Code	Course
1	17ECT641	Operations Research
2	17ECT642	Robotics
3	17ECT643	Internet of Things (IoT)

Open Elective-VIII

Sl. No.	Course Code	Course
1	17HOE661	LabVIEW – Level 1
2	17HOE662	Yoga and Meditation
3	17HOE663	Martial Arts
4	17HOE664	Music (Carnatic Vocal / Instrumental)
5	17HOE665	Dance
6	17HOE666	Sports
7	17HOE667	Online Certification Courses from IITs / IISc / SWAYAM / EDX

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Seventh Semester B.E. – Scheme

Sl. No.	Course Code	Course	TeachingDept.	L-T-P-S (Hrs/week)	Total Credits	Marks
1	16ECT71	Power Electronics	EC	3-0-0-0	3	100
2	16ECT72	Data Communication	EC	3-0-0-0	3	100
3	16ECI73X	Foundation Elective-X (IC)	EC	3-0-2-0	4	100
4	16ECT74X	Engineering Elective-XI	EC/ME/CS	3-0-0-0	3	100
5	16HOE75X	Open Elective-XII	EC/BS&H	2-0-0-4	3	100
6	16HOE76X	Open Elective-XIII	EC/BS&H	2-0-0-4	3	100
7	16ECL77	Power Electronics Lab	EC	1-0-2-0	2	100
8	16ECL78	Data Communication Lab	EC	1-0-2-0	2	100
9	16ECP79	Project Phase-I and Seminar	EC	0-0-6-0	3	100
Total				18-0-12-8	26	900

Foundation Elective-X (IC)

Sl. No.	Course Code	Course
1	16ECI731	Optical Fiber Communication
2	16ECI732	Web Technology
3	16ECI733	Online Certification courses from IITs / IISc / SWAYAM / EDX

Engineering Elective-XI / PBL

Sl. No.	Course Code	Course
1	16ECT741	Wireless Communication
2	16ECT742	Artificial Intelligence
3	16ECT743	MEMS

Open Elective-XII

Sl. No.	Course Code	Course
1	16HOE751	Tax Management
2	16HOE752	Assessment of Building Energy Performance (Of-fered by ASHRAE)
3	16HOE753	Crisis Management
4	16HOE754	Online certification courses from IITs / IISC /SWAYAM / EDX

Open Elective-XIII

Sl. No.	Course Code	Course
1	16HOE761	Small & Medium Enterprise Management
2	16HOE762	Occupational Safety & Health Administration
3	16HOE763	Animation & Multimedia Engineering
4	16HOE774	Online certification courses from IITs / IISC /SWAYAM / EDX

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Eighth Semester B.E. – Scheme

Sl. No.	Course Code	Course	Teaching Dept.	Total Credits	Marks
1	16ECP81	Project Phase-II and Seminar	EC	4	100
2	16ECP82	Project Phase-III and Seminar	EC	4	100
3	16ECP83	Evaluation and Viva voce (External)	EC	10	100
Total				18	300

Communication Systems

Course Code	L:T:P:S	Credits	Exam marks	Exam Duration	Course Type
17ECT51	3:0:0:0	3	CIE:50 SEE:50	3 hours	FC

Course Objectives:

This course will enable students to :

- Understand the concept of AM and DSBSC generation and demodulation.
- Analyze the generation and demodulation of SSB and VSB.
- Illustrate the concept of FM generation and demodulation.
- Analyze the effect of noise in CW modulation systems.

Syllabus

Module – I

Amplitude Modulation: Introduction to AM, Time-Domain description, Frequency – Domain description. Generation of AM wave, square law modulator, switching modulator. Detection of AM waves, square law detector, envelop detector. Double side band suppressed carrier modulation (DSBSC), Time-Domain description, Frequency-Domain representation, Generation of DSBSC waves, balanced modulator, ring modulator. Coherent detection of DSBSC modulated waves. **08 Hours**

Module – II

Single Side-Band Modulation (SSB) and Hilbert Transform: Properties of Hilbert transform, Pre-envelope. Single side-band modulation (SSB): Frequency-Domain description of SSB wave, Time-Domain description. Phase discrimination method for generating an SSB modulated wave, Demodulation of SSB waves.

Vestigial Side-Band Modulation (VSB): Frequency – Domain description, Generation of VSB modulated wave, Time – Domain description, Envelop detection of VSB wave plus carrier, Frequency translation, Application: AM radio. **08 Hours**

Module – III

Angle Modulation: Basic definitions, FM, narrow band FM, wide band FM, transmission bandwidth of FM waves, generation of FM waves: indirect FM and direct FM. frequency stabilization in FM receivers. **08 Hours**

Module – IV

Demodulation (FM): Demodulation of FM waves, Phase-locked loop (PLL), Linear model of the phase – locked loop, Nonlinear model of the phase – locked loop, Nonlinear effects in FM systems. **08 Hours**

Module – V

Noise In Continuous Wave Modulation Systems: Receiver model, Noise in DSB-SC receivers, Noise in SSB receivers, Noise in AM receivers, threshold effect, Noise in FM receivers, FM threshold effect, Pre-emphasis and De-emphasis in FM. **08 Hours**

Course Outcomes:

On successful completion of this course, students will be able to:

- Determine the generation and demodulation of AM and DSBSC systems.
- Understand the generation and demodulation of SSB, VSB and employ AM radio system.
- Describe the direct and indirect method of generation of FM.
- Evaluate the FM radio systems and its detection.
- Analyze the noise performance of receivers.

Text Books:

1. Simon Haykins: “Communication Systems”, John Willey India Pvt. Ltd., 5th Edition, 2009, ISBN: 9971-51-170-3.
2. Simon Haykins: “An Introduction to Analog and Digital Communication”, John Wiley India Pvt. Ltd., 2008, ISBN: 987-81-265-0932-4.

Reference Books:

1. B. P. Lathi: “Modern digital and analog Communication systems”, 4th Edition, Oxford University Press, 2010, ISBN: 0-195-68622-5.
2. Singh, Sapre: “Communication Systems Analog and digital”, 2nd Edition, TMH , 2007, ISBN: 100-07-063454-8.

E-Resources :

1. <http://www.radio-electronics.com/info/rf-technology-design/am-amplitude-modulation/single-sideband-ssb-modulation.php>
2. <https://electronicspost.com/explain-the-generation-of-am-waves-using-square-law-modulator-and-switching-modulator/>
3. https://www.tutorialspoint.com/principles_of_communication/principles_of_communication_vsb_modulation.htm
4. <http://www.radio-electronics.com/info/rf-technology-design/fm-reception/fm-demodulation-detection-overview.php>

Microcontrollers

Course Code	L:T:P:S	Credits	Exam marks	Exam Duration	Course Type
17ECT52	3:0:0:0	3	CIE:50 SEE:50	3 Hours	FC

Course Objectives:

This course will enable students to :

- Understand the microcontroller systems and learn the assembly level programming language.
- Understand the architecture of 8051 and MSP430 microcontroller.
- Understand with the 8051 and MSP430 microcontroller instruction set, registers.
- Understand with 8051 microcontroller subsystems, such as timer modules.
- Understand the microcontrollers with common peripheral devices, such as switches, visual displays.
- Understand the interfacing of external devices connected to the microcontroller using a standard bus.

Syllabus

Module - I

Microprocessors and Microcontrollers: Introduction, Microprocessors and Microcontrollers, A microprocessors survey, RISC and CISC CPU architectures, Harvard and Von-Neumann CPU architectures.

The 8051 Architecture: Introduction, Architecture of 8051, Pin diagram of 8051, Memory organization, External Memory interfacing, stacks.

Addressing Modes: Introduction, Instruction syntax, Data types, Subroutines, Addressing modes: Immediate addressing, Register addressing, direct addressing, indirect addressing, relative addressing, absolute addressing, Long addressing, Indexed addressing, Bit inherent addressing, bit direct addressing. **08 Hours**

Module – II

Instruction set: Instruction timings.

8051 instructions: Data transfer instructions, Arithmetic instructions, Logical instructions, Branch instructions, Subroutine instructions, Bit manipulation instruction.

8051 programming: Assembler directives, Assembly language programs and Time delay calculations, Assembly language programs. **08 Hours**

Module – III

8051 Interrupts and Timers/counters: Basics of interrupts, 8051 interrupt Structure

8051 Interfacing and Applications: Basics of I/O concepts, I/O Port Operation, Interfacing 8051 to LCD, Keyboard, parallel and serial ADC, DAC, Stepper motor interfacing and DC motor interfacing and programming. **08 Hours**

Module – IV

Timers and Counters: 8051 timers/counters, programming 8051 timers in assembly and C. Time delay calculations (without using timers).

8051 Serial Communication: Data communication, Basics of Serial Data Communication, 8051 Serial Communication, connections to RS-232, Serial communication Programming in assembly and C.

8255A Programmable Peripheral Interface: Architecture of 8255A, I/O addressing, I/O devices interfacing with 8051 using 8255A. **08 Hours**

Module – V

Motivation for MSP430 microcontrollers: Low Power embedded systems, On-chip peripherals (analog and digital), low power RF capabilities, Target applications (Single-chip, low cost, low power, high performance system design).

MSP430 RISC CPU architecture: Compiler-friendly features, Instruction set, Clock system, Memory subsystem. Key differentiating factors between different MSP430 families.

Digital I/O-I/O ports: Programming using C and assembly, Understanding the muxing scheme of the MSP430 pins. Addressing modes and Instruction set of MSP430.

08 Hours

Course Outcomes:

On successful completion of this course, students will be able to:

- Solve basic binary math operations using the microcontroller.
- Demonstrate programming proficiency using the various addressing modes and data transfer instructions of the target Microcontroller.
- Analyze program using the capabilities of the stack, the program counter, and the status register and show how these are used to execute a machine code program.
- Apply knowledge of the microcontroller's internal registers and operations by use of a PC based microprocessor Simulator and write assemble assembly language programs.
- Design electrical circuitry to the microcontroller I/O ports in order to interface the processor to external devices.

Text Books:

1. Muhammad Ali Mazidi, Janice Gillespie Mazidi, Rollin D. McKinley: “The 8051 Microcontroller and Embedded Systems – using assembly and C”, Pearson Publication, New Delhi, revised 2nd Edition, 2011, ISBN: 978-81-317-5899-1.
2. John Davies: “MSP430 Microcontroller Basics”, Elsevier, 2008, ISBN: 978-0-7506-8276-X.

Reference Books:

1. Kenneth J.Ayala: “The 8051 Microcontroller Architecture, Programming & Applications”, revised 3rd Edition, Thomson Learning, 2005, ISBN: 81-315-0200-7.
2. V.Udayashankar and Malikarjuna Swamy: “The 8051 Microcontroller”, Tata McGraw-Hill Education, 2009, ISBN: 978-0-07-008681-4.

E-Resources:

1. <https://www.elprocus.com/types-interfacing-devices-applications-with-microcontroller/>
2. <http://www.edsim51.com/8051Notes/8051/serial.html>
3. https://www.tutorialspoint.com/microprocessor/microprocessor_intel_8255a_programmable_peripheral_interface.html
4. <https://www.engineersgarage.com/tutorials/8051-assembly-programs>
5. <https://www.edgefx.in/8051-microcontroller-architecture/>



Information Theory and Coding

Course Code	L:T:P:S	Credits	Exam marks	Exam Duration	Course Type
17ECT53	3:0:0:0	3	CIE:50 SEE:50	3 Hours	FC

Course Objectives:

This course will enable students to :

- Measure the information content of a Message.
- Understand the coding techniques using different Algorithms.
- Understand the different types of channels and its characteristics
- Understand the different types of Errors and introduce Error control coding.

Syllabus

Module – I

Information Theory: Introduction, Measure of information, Average information content of symbols in long independent sequences, Mark off statistical model for information source, Entropy and information rate of mark-off source. **08 Hours**

Module – II

Source Coding: Encoding of the source output, Shannon’s encoding algorithm. Shannon Fano Encoding Algorithm, Huffman coding, Introduction to Communication Channels. **08 Hours**

Module – III

Introduction to Error Control Coding: Introduction, Types of errors, examples, Types of codes Linear Block Codes: Matrix description, Error detection and correction, Encoder for Linear block codes, Syndrome Calculation Circuit for Linear block codes. **08 Hours**

Module – IV

Binary Cyclic codes, Algebraic Structure of Cyclic codes, Encoding using an (n-k) bit shift register, Syndrome Calculation, BCH codes. **08 Hours**

Module – V

Convolution codes, Time domain approach, Transform domain approach, tree diagram, RS Codes. **08 Hours**

Course Outcomes:

On completion of this course, students will be able to :

- Compute entropy and information rate of a source.
- Encode the source output using encoding algorithms and coding techniques.
- Determine the channel capacity of different channels and also the mutual information.
- Implement the error control coding, methods of controlling errors and Error correction and detection.
- Encode using bit shift register, syndrome calculate and complete knowledge of BCH and burst error correcting codes.

Text Books:

1. K. Sam Shanmugam: "Digital and Analog Communication Systems", John Wiley India Pvt. Ltd., 2008, ISBN-10: 8126509147, ISBN-13: 9788126509140.
2. Simon Haykin: "Digital Communication", John Wiley India Pvt. Ltd., 2008. ISBN-10: 0471647357, ISBN-13: 978-0471647355.

Reference Books:

1. Dr. P. S. Satyanarayana: "Concepts of Information Theory & Coding", Publication, Dynaram, 2005, ISBN-13:1234567150966
2. Bernard Sklar, Digital Communications Fundamentals and Applications, Prentice Hall International, 2001, ISBN-10: 0130847887, ISBN-13: 978-0130847881.
3. Shu Lin, Costello, "Error Control coding : Fundamentals and Applications", New Jersey, 1983, ISBN-10: 0130426725, ISBN-13: 978-0130426727.

E- Resources:

1. <https://users.cs.cf.ac.uk/Dave.Marshall/Multimedia/node217.html>
2. https://users.ece.cmu.edu/~koopman/des_s99/coding/
3. https://www.cs.cmu.edu/~guyb/realworld/reedsolomon/reed_solomon_codes.html
4. www.geeksforgeeks.org/greedy-algorithms-set-3-huffman-coding/
5. <https://www.kernel.org/doc/html/docs/librs/ch03s03.html>



Control Systems (IC)

Course Code	L:T:P:S	Credits	Exam marks	Exam Duration	Course Type
17ECI541	3:0:2:0	4	CIE:50 SEE:50	3 Hours	FC

Course Objectives:

This course will enable students to :

- Understand the mathematical modelling of mechanical and electrical systems.
- Understand the concept of block diagram reduction techniques and signal flow graph to find the transfer function of a given system.
- Understand the time response of first and second order systems for different test input signals.
- Understand the concept of stability of control systems and stability analysis using RH.
- Understand the Criterion and Nyquist Criterion.
- Understand the concept of root locus in the construction of root loci in order to determine the stability of a given transfer functions.
- Understand the frequency response concepts for assessment of relative stability using Bode plots.

Syllabus

Module - I

Modelling of Systems: The control system, Mathematical models of Physical systems, Introduction, Differential equations of physical systems - Mechanical systems, Translational systems, Rotational systems, Electrical systems, Analogous systems.

Block diagrams and signal flow graphs: Transfer functions, Block diagram algebra and Signal Flow graphs. **08 Hours**

Module – II

Time response of feedback control systems: Standard test signals, Unit step response of First and second order systems, Time response specifications. Steady state error and error constants. Applications: Design and stability of second order system. **08 Hours**

Module – III

Stability analysis: Concept of stability, Routh-Hurwitz criterion, Relative stability analysis, application of Routh stability criterion, Nyquist plot: polar plots, Nyquist stability criterion, assessment of relative stability using Nyquist criterion. **08 Hours**

Module – IV

Root-Locus Techniques: Introduction, the root locus concepts, Construction of root loci, numerical examples. **08 Hours**

Module – V

Frequency response analysis: Introduction, Bode diagrams, assessment of relative stability using Bode plots.

Introduction to State variable analysis: Concepts of state, state variable and state models for electrical systems, Solution of state equations. **08 Hours**

List of Experiments:

1. Determine the transfer function for given closed loop system in block diagram representation.
2. Plot unit step response of given transfer function and find peak overshoot, peak time.
3. Plot unit step response and to find rise time and delay time.
4. Plot locus of given transfer function, locate closed loop poles for different values of k .
5. Plot root locus of given transfer function and to find out S , W_d , W_n at given root & to discuss stability.
6. Plot bode plot of given transfer function

Course Outcomes:

On completion of this course, students will be able to :

- Employ mathematical modelling techniques to determine the transfer function of a given system.
- Analyze the time response of first and second order systems for different test input signals.
- Apply the concept of RH criterion and Nyquist criterion to determine the stability of a given transfer functions.
- Interpret the concept of root locus to determine the stability of a given transfer function.
- Know the frequency domain specification fundamentals and sketch a Bode plot to analyze Stability of a given systems and able to write state model for the given system.

Text Books:

1. J. Nagarath, M.Gopal: "Control Systems Engineering", 5th Edition, New Age International (P) Limited, New Delhi, 2007, ISBN: 812242087.
2. K. Ogata: "Modern Control Engineering - Pearson PHI, 5th Edition- 2010, ISBN: 9788120340107.

Reference Books:

1. Benjamin C. Kuo, Farid Golnaaghi: "Automatic Control Systems", 8th Edition, Wiley student Edition, 2010, ISBN: 9788126513710.
2. A K Jairath: "Problems and solutions of control systems with essential theory", Reprint 5th Edition, CBS, New Delhi, 2009, ISBN: 978-81-239-1686-6.

E- Resources:

1. <https://www.electrical4u.com/signal-flow-graph-of-control-system/>
2. <http://ipsa.swarthmore.edu/Bode/BodeReviewRules.html>
3. <http://nptel.ac.in/courses/10810308/25>
4. <https://www.electrical4u.com/root-locus-technique-in-control-system-root-locus-plot/>
5. <http://www.facstaff.bucknell.edu/mastascu/econtrolhtml/rootlocus/rlocus1a.html>

Low Power VLSI Design (IC)

Course Code	L:T:P:S	Credits	Exam marks	Exam Duration	Course Type
17ECI542	3:0:2:0	4	CIE:50 SEE:50	3 Hours	FC

Course Objectives:

This course will enable students to :

- Understand the physics of power dissipation.
- Understand the basic sources of power dissipation in VLSI.
- Understand the basics of synthesis for low power.
- Understand the concept of leakage power in deep submicron transistors.
- Understand the basics of low energy computing.

Syllabus

Module - I

Introduction, Sources of power dissipation, designing for low power. Physics of power dissipation in MOSFET devices – MIS Structure, Long channel and sub-micron MOSFET. **08 Hours**

Module – II

Power dissipation in CMOS: Power dissipation in CMOS – Short circuit dissipation, dynamic dissipation, Load capacitance. Low power design limits - Principles of low power design, Hierarchy of limits, fundamental limits, Material, device, circuit and system limits. **08 Hours**

Module – III

Synthesis for low power: Behavioral, Logic and Circuit level approaches, Algorithm level transforms, FSM and Combinational logic, Transistor sizing. **08 Hours**

Module – IV

Design and test of low-voltage cmos circuits: Introduction, Design style, Leakage current in Deep sub-micron transistors, device design issues, Testing with elevated intrinsic leakage, multiple supply voltages. **08 Hours**

Module – V

Low energy computing: Energy dissipation in transistor channel, Energy recovery circuit design, designs with reversible and partially reversible logic, energy recovery in adiabatic logic and SRAM core.

Software design for low power: Introduction, sources of power dissipation, power estimation and optimization. **08 Hours**

List of Experiments:

1. Full adder
2. Ripple Carry Adder (RCA)
3. Carry Skip Adder (CSkA)
4. Conventional Multiplier
5. Booth multiplier
6. Wallace tree multiplier
7. Array multiplier
8. Sequential multiplier

Course Outcomes:

On completion of this course, students will be able to :

- Distinguish between different types of MOSFET devices.
- Analyze different principles of low power VLSI design
- Apply the concept of transistor sizing for synthesis of low power.
- Design and test of low-voltage CMOS circuits
- Estimate the power consumption of VLSI circuits & optimize it

Text Book:

1. Kaushik Roy, Sharat C Prasad: “Low-Power CMOS VLSI Circuit Design”, Wiley, 2009, ISBN-10: 812652023X, ISBN-13: 978-8126520237.

Reference Book:

1. Weste, Harris: “CMOS VLSI Design: A circuits and systems perspective”, 4th Edition, Pearson Education India, 2015, ISBN-10: 9332542880, ISBN-13: 978-9332542884.

E- Resources:

1. <http://www.iue.tuwien.ac.at/phd/schrom/node89.html>
2. <https://www.cs.umd.edu/class/sum2003/cmsc311/Notes/Seq/fsm.html>
3. <http://ecomputernotes.com/mis/structure-and-classification/describe-the-mis-structure-based-on- the-physical-components>
4. <http://large.stanford.edu/courses/2010/ph240/iyer2/>



Microwaves and RADAR (IC)

Course Code	L:T:P:S	Credits	Exam marks	Exam Duration	Course Type
17ECI543	3:0:2:0	4	CIE:50 SEE:50	3 Hours	FC

Course Objectives:

This course will enable students to :

- Understand the concepts of Transmission lines and its applications using Smith chart.
- Understand the rectangular waveguides and its modes of operation.
- Understand the Directional Couplers and have a basic idea about circulators and isolators.
- Understand the concept of GUNN diode and its modes of operation.
- Understand the concept of Strip lines and Microstrip lines
- Understand the origins of RADAR and its applications.

Syllabus

Module – I

Microwave Transmission Lines: Introduction, Transmission Lines, Equations and Solutions, Reflection and Transmission Coefficients, Standing Waves and SWR, Smith Chart, Impedance matching using Single Stubs, Microwave coaxial connectors and Adapters.

Microwave Waveguides and Components: Rectangular waveguides, Circular Waveguides, Directional Couplers, Circulators and Isolators, Reflex Klystrons.

08 Hours

Module – II

Microwave Diodes: Transfer electron devices: Introduction, GUNN effect diodes- GaAs diode, RWH theory, Modes of Operation, Avalanche transit time devices: READ diode, IMPATT diode, parametric amplifiers, and other diodes: PIN diodes. **08 Hours**

Module – III

Microwave Network Theory and Passive Devices: Symmetrical Z and Y parameters for reciprocal networks, S matrix representation of Multi port networks, Phase shifters, Attenuators, Waveguide Tees, Magic Tees. **08 Hours**

Module – IV

Strip Lines: Introduction, Micro strip lines, Parallel strip lines, coplanar strip lines, shielded strip lines. **07 Hours**

Module – V

An Introduction to Radar: Basic Radar, The simple form of the Radar equation, Radar block diagram, radar frequencies, applications and origins of Radar.

MTI and Pulse Doppler Radar: Introduction to Doppler and MTI Radar, Delay line Cancellers, digital MTI processing, Moving Target Detector, pulse Doppler Radar.

08 Hours

List of Experiments:

1. To conduct an experiment to obtain the mode curves of Reflex Klystron.
2. To conduct an experiment to find power division and isolation of a Magic Tee.
3. Measurement of Frequency, Wavelength, Power, VSWR and Attenuation in a microwave test bench using Klystron.
4. Determination of Coupling and Isolation characteristics of a Stripline-Directional Coupler.
5. To conduct an experiment to determine the V-I curve of a GUNN diode, measurement of a guide wavelength (λ_g), frequency and VSWR.
6. Measure of power division and isolation characteristics of a microstrip 3 dB power divider.

Course Outcomes:

On completion of this course, students will be able to :

- Define line parameters and analyze various transmission lines and resonators.
- Understand the basic concepts of diodes and its applications.
- Apply the concepts of S parameters to analyze waveguide Tees.
- Analyze the differences between various strip lines and its applications
- Apply the concepts of RADAR to find range of the target object and velocity of the target.

Text Books:

1. Samuel Y Liao: “Microwave Devices and Circuits”, 3rd Edition, Pearson, 2011, ISBN-10: 0135832047, ISBN-13: 978-0135832042.
2. Merill I Skolnik: “Introduction to Radar systems”, 3rd Edition, McGraw-Hill Education, 2002, ISBN-10: 0072881380, ISBN-13: 978-0072881387
3. Annapurna Das, Sisir K Das: “Microwave Engineering”, TMH Publication, 3rd Edition, 2014, ISBN-10: 9332902879, ISBN-13: 978-9332902879

Reference Book:

1. David M Pozar: “Microwave Engineering”, 4th Edition, Wiley, 2011, ISBN-10: 0470631554, ISBN-13: 978-0470631553

E- Resources:

1. <https://www.maximintegrated.com/en/app-notes/index.mvp/id/742>
2. <https://www.elprocus.com/gunn-diode-working-characteristics-and-its-applications/>
3. <http://www.radartutorial.eu/11.coherent/co13.en.html>
4. <http://www.rfwireless-world.com/Articles/types-and-basics-of-microstrip-line.html>
5. <https://www.microwaves101.com/encyclopedias/magic-tees>
6. <http://www.radio-electronics.com/info/antennas/waveguide/waveguide-basics-tutorial.php>



Digital System Design using Verilog (IC)

Course Code	L:T:P:S	Credits	Exam marks	Exam Duration	Course Type
17ECI552	3:0:2:0	4	CIE:50 SEE:50	3 Hours	FC

Course Objectives:

This course will enable students to :

- Understand the fundamental design methodologies in Verilog and combinational basics.
- Understand the concepts of number system basics and sequential basics.
- Understand the Computer organization, Instruction and Data.
- Understand the concept of I/O interfacing, accelerators and design methodologies.

Syllabus

Module – I

Introduction and Methodology: Digital Systems and Embedded Systems, Binary representation and Circuit Elements, Real-World Circuits, Models, Design Methodology.

Combinational Basics: Boolean Functions and Boolean Algebra, Binary Coding, Combinational Components and Circuits, Verification of Combinational Circuits.

08 Hours

Module – II

Number Basics: Unsigned and Signed Integers, Fixed and Floating-point Numbers.

Sequential Basics: Storage elements, Counters, Sequential Data paths and Control, Clocked Synchronous Timing Methodology.

08 Hours

Module – III

Memories: Concepts, Memory Types, Error Detection and Correction. Implementation Fabrics: ICs, PLDs, Packaging and Circuit Boards, Interconnection and Signal Integrity.

Processor Basics: Embedded Computer Organization, Instruction and Data, Interfacing with memory.

08 Hours

Module – IV

I/O interfacing: I/O devices, I/O controllers, Parallel Buses, Serial Transmission, I/O software.

08 Hours

Module – V

Accelerators, Design Methodology: Accelerators: Concepts, case study, Verification of accelerators. Design Methodology: Design flow, Design optimization, Design for test. **08 Hours**

List of Experiments:

1. Write the hardware description of a 4-bit PRBS (pseudo-random Binary sequence) generator using a linear feedback shift register and test it
2. Write the hardware description of a 8-bit register with shift left and shift right modes of operation and test its operation
3. Write the hardware description of a 8-bit register with parallel load and shift left modes of operation and test its operation.
4. Write the hardware description of a 4-bit mod-13 counter and test it.
5. Write the hardware description of a 4-bit adder/subtractor and test it.
6. Write the hardware description of a carry look ahead, ripple carry adder and test it
7. Write the hardware description of booth multiplier and test it
8. Write the hardware description of Wallace tree multiplier and test it

Course Outcomes:

On completion of this module, students will be able to :

- Analyze and verify various combinational circuits.
- Discuss number basics and sequential basics in Verilog.
- Analyze the appropriate usage of instructions and data types.
- Analyze various I/O interfacing and software.
- Describe and verify accelerators and design methodologies.

Text Books:

1. Peter J Ashenden: “Digital Design: An embedded system approach using verilog”, 1st Edition, Elsevier, 2010, ISBN: 9780123695277.

Reference Books:

1. Samir Palnitkar: “Verilog HDL”, 2nd Edition, Pearson, New Delhi, 2003, ISBN: 978-0132599702.

E-Resources:

1. <http://esd.cs.ucr.edu/labs/tutorial/>
2. <http://www.thomasnet.com/products/printed-circuit-board-pcb-packaging-55661177-1.html>
3. <https://learn.sparkfun.com/tutorials/serial-communication>
4. http://www.people.vcu.edu/~rhklenke/tutorials/actel/design_flow.html

Object Oriented Programming with JAVA

Course Code	L:T:P:S	Credits	Exam marks	Exam Duration	Course Type
17ECI552	3:0:2:0	4	CIE:50 SEE:50	3 Hours	FE

Course Objectives:

This course will enable the students to :

- Understand the basic concepts of Java Technology and its features.
- Understand the OOPs concepts.
- Learn methods for solving programs in Java.
- Effectively use data structures like Collections, Lists, etc.
- Write defensive programming using Exception Handling.

Syllabus

Module – I

Introduction to Java: Why Java, Flavors of Java, Java Designing Goal and Features, JVM / JDK / JRE / History of JDK / JDM, Usage of IDE (Eclipse, NetBeans)

Language Fundamentals: Data Types - Variables, keywords, operators; Selection / Iterative / Decision making statements

Introduction to OOPs Concepts: Inheritance - Polymorphism - Abstraction – Encapsulation **10 Hours**

Module – II

Arrays and Strings: Defining of an Array, Initializing and accessing an Array, Multi-Dimensional Array, String / StringBuffer / StringBuilder

OOPs in Java: Inheritance, Abstract class and interface, Abstract class Vs Interface

Packages and Wrapper Classes: Defining Package, Organizing Classes and interfaces in Packages, Package as Access Protection, Import and Static Import, Naming Convention for packages, What is Wrapper Class, Why Wrapper, How to handle wrapper Classes. **10 Hours**

Module – III

Exception Handling: What is Exception, Types of Exception, Exception Hierarchy, Custom exceptions.

The Collection Framework: Collection of objects, Collection Interfaces and Hierarchy, List and Map, Types of List, Types of Map, Iterator, Generics. **08 Hours**

Module – IV

Threads: Understanding Threads, Needs of Multi-Threaded Programming, Thread Life-cycle, Synchronizing Threads. **02 Hours**

Module – V

Project Work: To create a Contact Book application using the Core Java concepts learnt with special emphasis on OOPs concepts, Exception Handling, and Collections Framework. **10 Hours**

Laboratory

1. Programs covering Data Types and OOPs Concepts.
2. Programs covering Arrays and Strings, OOPS concepts in Java, Packages and Wrapper Classes.
3. Programs covering Exception Handling, The Collection Framework and Threads.

Course Outcomes:

On completion of this course, students will be able to :

- Explain the basic concepts of Java Technology and its features.
- Explain the OOPs concepts.
- Write programs in Java.
- Analyze data structures like Collections, Lists, etc.
- Write defensive programming using Exception Handling.

Text Books:

1. Herbert Schildt: “Java: The Complete Reference”, McGraw Hill Education, 9th Edition, ISBN-10: 9339212096.
2. Dr. R. Nageswara Rao: “Core Java: An Integrated Approach”, 1st Edition, Dreamtech Press, 2016, ISBN-10: 9351199258.

Reference Books:

1. Joshua Bloch: “Effective Java”, Pearson Education, 2nd Edition, ISBN-10: 933257653X.
2. Cay S. Horstmann: “Core Java - Vol. I - Fundamentals”, 10th Edition, Pearson Education, ISBN-10: 9332582718.



Mechatronics

Course Code	L:T:P:S	Credits	Exam marks	Exam Duration	Course Type
17ECT561	3:0:0:0	3	CIE:50 SEE:50	3 Hours	EE

Course Objectives:

This course will enable students to :

- Mechanical, electronics, control, software, and computer engineering, and a solid command of the newest technologies.

Syllabus

Module – I

Introduction to mechatronics: Measurement and control systems. Their elements and functions, Microprocessor based controllers. Definition and classification of transducers. Definition and classification of sensors. Principle of working and applications of light sensors, proximity sensors and Hall effect sensors. **08 Hours**

Module – II

Electrical Actuation Systems: Electrical systems, Mechanical switches, solid-state switches, solenoids, DC & AC motors, Stepper motors and their merits and demerits **07 Hours**

Module – III

Signal Conditioning: Introduction to signal conditioning. The operational amplifier, Protection, Filtering, Wheatstone bridge, Digital signals Multiplexers, Data acquisition, Introduction to Digital system. Processing Pulse-modulation. **08 Hours**

Module – IV

Logic Function: Data word representation. Basic elements of control systems 8085A processor architecture terminology such as CPU, memory and address, ALU, assembler data registers, Fetch cycle, write cycle, state, bus, interrupts. Micro Controllers. Difference between microprocessor and micro controllers. Requirements for control and their implementation in micro controllers. Classification of micro controllers. **08 Hours**

Module – V

Programming of Microprocessors: Introduction to organization of INTEL 8085-Data and Address buses, Instruction set of 8085, programming the 8085, assembly language programming.

Organization and Programming of Microprocessors: Introduction to organization of INTEL 8085-Data and Address buses, Instruction set of 8085. **08 Hours**

Course Outcomes:

On completion of this course, students will be able to :

- Design and conduct experiments to evaluate the performance of a mechatronics system
- Design mechatronics component, system or process to meet desired needs
- Use the techniques, skills, and modern mechatronics engineering tools necessary for engineering practice.
- Identify and evaluate ethical ramifications and professional responsibilities in a variety of situations
- Discuss the impact of engineering on society, safety, and environment in relation to contemporary issues

Text Books:

1. W.Bolton: "Mechatronics", Pearson, 5th Edition, 2011, ISBN-10: 0273742868, ISBN-13: 978-0273742869.
2. R.S. Ganokar : "Microprocessor Architecture, Programming and Applications with 8085/8085A", Wiley Eastern, ISBN-10: 8187972882, ISBN-13: 978-8187972884.

Reference Books:

1. K.P.Ramchandran, G.K.Vijayraghavan, M.S.Balasundran: "Mechatronics and Micro processors", Wiley, 1st Edition, 2009.
2. Nitaigour, Premchand Mahilik: "Mechatronics - Principles, Concepts and applications", Tata McGraw Hill, 2003.
3. Godfrey C. Onwubolu: "Mechatronics Principles and applications", Elsevier.
4. David. G. Aliciatore, Michael. B. Bihistaned: "Introduction Mechatronics & Measurement systems", Tata McGraw Hill, 2000.

E-Resources:

1. <https://lecturenotes.in/topic/2760/mechatronics/electrical-actuation-systems/?page=12>
2. <http://efxkits.com/blog/what-is-multiplexer-and-types/>
3. <http://www.electronics-tutorials.ws/electromagnetism/hall-effect.html>



Energy Engineering and Management

Course Code	L:T:P:S	Credits	Exam marks	Exam Duration	Course Type
17ECT562	3:0:0:0	3	CIE:50 SEE:50	3 Hours	EE

Course Objectives:

This course will enable students to :

- The various types of Power plants and applications
- The concept of electrical and thermal energy management

Syllabus

Module – I

Steam Power Plant: Different Types of Fuels used for steam generation, Equipment for burning coal in lump form, stokers, different types, Oil burners, Advantages and Disadvantages of using pulverized fuel, Equipment for preparation and burning of pulverized coal, unit system and bin system. Pulverized fuel furnaces, cyclone furnace, Coal and ash handling, Generation of steam using forced circulation, high and supercritical pressures. **08 Hours**

Module – II

Diesel Engine Power Plant: Applications of Diesel Engines in Power field. Method of starting Diesel engines. Auxiliaries like cooling and lubrication system, filters, centrifuges, Oil heaters, intake and exhaust system, Layout of diesel power plant. **07 Hours**

Module – III

Energy and its various forms: Commercial and Non-commercial energy, primary energy resources, commercial energy production, Energy pricing, energy security, energy conservation and its importance, Electricity tariff, load management and maximum demand control, Thermal energy contents of fuel, heat capacity, sensible and latent heat, heat transfer, Stoichiometric air-fuel ratio, Flue gas analysis. **08 Hours**

Module – IV

Electrical Energy Management: Reactive power management, Energy conservation in domestic and industrial sectors, Energy conservation in lighting, motors, pumps and fan systems. **08 Hours**

Module – V

Thermal Energy Management: Energy conservation in boilers and Furnaces, Waste heat recovery, Thermal insulation, Energy conservation in buildings, Building heating and cooling load management, Buildings code, solar passive and green building concepts. **08 Hours**

Course Outcomes:

On successful completion of this course, students will be able to:

- Describe the technology behind Steam Power plants and the types of fuels used.
- Understand and describe diesel engine power plants, and the auxiliaries used.
- Differentiate between the various types of Energy forms.
- Understand the management of electric energy.
- Describe the methods used in thermal energy management and energy conversation.

Text Book:

1. P.K.Nag: “Power plant engineering”, 2nd Edition, Tata McGraw Hill, ,2001, ISBN-10: 9339204042, ISBN-13: 978-933920404.

Reference Book:

1. Domkundwar, Arora Domkundwar: “Power plant engineering”, Dhanpat Rai & Co. (P) Limited, 2016, ISBN-10: 8177001957, ISBN-13: 978-8177001952.

E-Resources:

1. <http://www.mechanicalbooster.com/2016/08/steam-power-plant.html>
2. <http://wgbis.ces.iisc.ernet.in/energy/paper/alternative/classification.html>
3. <http://www.maintenancetechnology.com/2006/01/energy-management-for-electrical-systems/>
4. <https://research.ncl.ac.uk/sustem/>



Linear Algebra

Course Code	L:T:P:S	Credits	Exam marks	Exam Duration	Course Type
17ECT563	3:0:0:0	3	CIE:50 SEE:50	3 Hours	EE

Course Objectives:

This course will enable students to :

- The principles of linear algebra, calculus of variations, probability theory and random process
- The knowledge of linear algebra, calculus of variations, probability theory and random process in the applications of electronics and communication engineering sciences

Syllabus

Module – I

Linear Equations and Matrices: Introduction, Systems of Linear Equations, Matrices, Matrix Multiplication, Algebraic Properties of Matrix Operations, Elementary row operations, row echelon form, solving a system of linear equations, homogeneous systems. **08 Hours**

Module – II

Solving Linear Systems: Echelon Form of a Matrix Solving Linear Systems, Elementary Matrices, Matrices and Gaussian Elimination, Gauss Jordan method, Inverses, LU-factorization, LDU- factorization. **08 Hours**

Module – III

Determinants: Definition, Properties of Determinants, Cramer's rule to solve simultaneous equations, Co-factor Expansion, Inverse of a Matrix, Formulas for the Determinant, Applications of Determinant. **08 Hours**

Module – IV

Inner Product Spaces: Inner products, Projections onto Lines, inner product spaces, orthogonal sets and projections, Gram-Schmidt Orthogonalization, Least Squares Approximations, QR Decomposition. **08 Hours**

Module – V

Eigen values and Eigen vectors: Introduction, characteristic equation, Diagonalization of a Matrix, Difference Equations and the Powers A^k , Differential Equations and the Exponential e^{At} . **08 Hours**

Course Outcomes:

On completion of this course, students will be able to :

- Understand vector spaces, basis, linear transformations and the process of obtaining matrix of linear transformations arising in magnification and rotation of images.
- Apply the techniques of QR and singular value decomposition for data compression, least square approximation in solving inconsistent linear systems.
- Utilize the concepts of functional and their variations in the applications of communication systems, decision theory, synthesis and optimization of digital circuits.
- Learn the idea of random variables (discrete/continuous) and probability distributions in analyzing the probability models arising in control systems and system communications.
- Apply the idea of joint probability distributions and the role of parameter dependent random variables in random process.

Text Books:

1. David C. Lay: "Linear Algebra and its Applications," 3rd Edition, Pearson Education (Asia) Pvt. Ltd., 2005.
2. Gilbert Strang: "Linear Algebra and its Applications", 3rd Edition, Thomson Learning Asia, 2003.

Reference Books:

1. Kenneth Hoffman, Ray Kunze: "Linear Algebra," 2nd Edition, Pearson Education (Asia) Pvt. Ltd., Prentice Hall of India, 2004.

E References:

1. <https://www.math.hmc.edu/calculus/tutorials/linearsystems/>
2. <http://nptel.ac.in/courses/122104018/node49.html>
3. <http://math.tutorvista.com/algebra/gauss-jordan-method.html>
4. https://www.math.drexel.edu/~jwd25/LM_SPRING_07/lectures/lecture4B.html



Microcontroller Lab

Course Code	L:T:P:S	Credits	Exam marks	Exam Duration	Course Type
17ECL57	1:0:2:0	2	CIE:50 SEE:50	3 hours	FC

Course Objectives:

This course will enable students to :

- Understand the basics of 8051 programming like data transfer, arithmetic and logical instructions
- Understand the interfacing used in 8051 microcontroller, like stepper motors, DC motors.

List of Experiments:

1. Data Transfer - Block move, Exchange, Sorting, Finding largest element in an array.
2. Arithmetic Instructions - Addition/subtraction, multiplication and division, square, Cube
3. Counters.
4. Logical instruction (Bit and Byte manipulations).
5. Code conversion: BCD – ASCII; ASCII – Decimal; Decimal - ASCII; HEX - Decimal and Decimal - HEX.
6. Programs using serial port.
7. Generate different wave forms Sine, Square, Triangular, Ramp, etc using DAC interface to 8051.
8. Stepper and DC motor control interface to 8051.
9. Alphanumeric LCD panel and Hex keypad input interface to 8051.
10. External ADC and Temperature control interface to 8051.
11. Elevator interface to 8051.

Course outcomes :

On completion of this course, students will be able to:

- Understand data moving and exchange programs.
- Analyze and write delay programs.
- Interface Stepper motor, DC motor to the microcontroller.
- Discuss seven segment display and keyboard interface.
- Analyze different types of code conversion programs.

Text Books:

1. Muhammad Ali Mazidi, Janice Gillespie Mazidi and Rollin D. McKinley..“The 8051 Microcontroller and Embedded Systems – using assembly and C”-, Pearson Publication, New Delhi, revised second edition, 2011, ISBN-978-81-317-5899-1.
2. The 8051 Microcontroller Architecture, Programming & Applications”, Kenneth J.Ayala; revised third edition, 2005, Thomson Learning 2005, ISBN-81-315-0200-7.
3. “The 8051 Microcontroller”, V.Udayashankar and Malikarjuna Swamy, Tata McGraw –Hill Education, 2009, ISBN-978-0-07-008681-4.



Communication System Lab

Course Code	L:T:P:S	Credits	Exam marks	Exam Duration	Course Type
17ECL58	1:0:2:0	2	CIE:50 SEE:50	3 Hours	FC

Course Objectives:

This course will enable students to :

- The practical perspective of various communication modules.
- The various analog modulation and demodulation techniques using discrete Components.

List of Experiments:

1. Design and construction of second order active low-pass filter and high-pass filter. Plot of frequency response and estimation of roll-off factor
2. Design and construction of active band-pass filter and band-stop filter. Plot of frequency response.
3. Amplitude modulation using transistor/FET (Generation and detection) and find the Modulation index.
4. Frequency modulation using IC 8038/2206.
5. Design and conduct an experiment on Half wave and Full wave rectifier
6. Design and conduct an experiment for Pre-emphasis and de-emphasis.
7. Design and conduct an experiment to generate Pulse amplitude modulation and demodulation.
8. Conduct an experiment to generate a PWM signal and demodulation.
9. Design and conduct an experiment to generate PPM signal.
10. Design and test an R-2R Ladder network and verify truth table.

Course Outcomes:

On completion of the course, students will be able to :

- Demonstrate second order active filters for various frequency bands
- Understand the design and implementation concept for modulation and demodulation circuit using amplitude modulation.
- Understand the design and implementation concept for modulation circuit using frequency modulation
- Analyze the circuit by conducting the precision rectifiers experiment

- Construct the circuit and demonstrate the characteristics of pre-emphasis and de-emphasis circuit.

Text Books:

1. Simon Haykins: "Communication Systems", John Willey, India Pvt. Ltd, 5th Edition, 2009. ISBN 9971-51-170-3.
2. B. P. Lathi: "Modern digital and analog Communication systems", Oxford University Press., 4th Edition, 2010, ISBN: 0-195-68622-5



General Aptitude

Course Code	L:T:P:S	Credits	Exam marks	Exam Duration	Course Type
17ECH59	2:0:0:0	2	CIE:50 SEE:50	3 Hours	HSS

Course Objectives:

This course will enable students to :

- Understand different types of Numerical / Arithmetical problems.
- Understand the different Data interpretation problems.

Syllabus

Module – I

Numerical Ability-I: Numbers, HCF and LCM of numbers, Decimal Fractions, Average, Problems on Numbers, Problems on Ages. **06 Hours**

Module – II

Numerical Ability-II: Percentage, Profit and Loss, Ratio and Proportion, Partnership, Chain Rule, Time and Work. **05 Hours**

Module – III

Numerical Ability-III: Pipes and Cistern, Time and Distance, Problems on Trains, Alligation or Mixture, Simple Interest, Compound Interest. **05 Hours**

Module – IV

Numerical Ability-IV: Races and Games of Skill, Calender, Clocks, Permutations and Combinations, Probability, Odd man out and Series. **05 Hours**

Module-V

Data Interpretation: Tabulation, Bar Graphs, Pie Charts, Line Graphs. **05 Hours**

Course Outcomes:

On completion of this course, students will be able to :

- Solve and analyze different types of Numerical / Arithmetical problems.
- Solve and analyze different Data interpretation problems.

Text Books:

1. R S Aggarwal: “Quantitative Aptitude for competitive examinations”, (Chapters 1-3,6-8,10-18,20-22,26-28,30,31,35-39), S. Chand Publishing, New Delhi, 2014, ISBN-13: 978-81-219-2498-6.



Sixth Semester B.E. – Syllabus

Digital Signal Processing

Course Code	L:T:P:S	Credits	Exam marks	Exam Duration	Course Type
17ECT61	4:0:0:0	4	CIE:50 SEE:50	3 Hours	FC

Course Objectives:

This course will enable students to :

- Understand the importance of Fourier Transform and its relation with other transform.
- Understand the filtering of long data sequence using various methods.
- Understand the concept of FFT algorithms to compute DFT.
- Understand the design of digital FIR filters using various window methods.
- Understand the basics of designing IIR filter using different methods.
- Understand the concept of Multi-rate signal processing and sample rate conversion.

Syllabus

Module – I

Discrete Fourier Transform: DFT as a linear transformation, its relationship with other transforms, Properties of DFT, Multiplication of two DFTs – circular convolution, DFT in linear filtering, Filtering long data sequences: overlap-save, Filtering long data sequences: overlap-add method. **10 Hours**

Module – II

FFT Algorithm: Introduction to Radix-2 Fast Fourier Transform(FFT), Decimation in Time FFT and Decimation in Frequency FFT. **10 Hours**

Module – III

FIR Filters: Symmetric and anti-symmetric FIR filters, Design of linear-phase FIR filters using windows and frequency sampling methods.

Structures for FIR Systems: Direct-Form Structures, Cascade-Form Structures and Lattice Structures. **10 Hours**

Module – IV

IIR Filters: Analog filter specifications, classification of analog filters: Butterworth and Chebyshev filters, frequency transformations, design of analog filters, Digital IIR filter design using impulse invariant, bilinear transformation, Matched Z-transform methods.

IIR Filter Structures: Direct form (I and II), Cascade, Parallel, and Transposed structures. **10 Hours**

Module – V

Multi-Rate Signal Processing Fundamentals: Basic sample rate alteration devices, Multi-Rate Structures for sampling rate Converters, Multistage design of decimator and Interpolator, Applications of Multirate Signal Processing. **10 Hours**

Course Outcomes:

On completion of this module, students will be able to :

- Implement DFT using linear filtering.
- Implement DFT using Fast Fourier Transforms.
- Design and analyze digital FIR filters and structure of FIR filters.
- Design and analyze digital IIR filters and structure of IIR filters.
- Explain the concept of Multi-rate signal processing and sample rate conversion.

Text Books:

1. J. G. Proakis, D. G. Manolakis: “Digital Signal Processing: Principles, Algorithms and Applications”, 4th Edition, Pearson Education Asia/Prentice Hall of India, 2002, ISBN-10: 0131873741, ISBN-13: 978-0131873742.
2. Sanjit K. Mitra: “Digital Signal Processing”, 4th Edition, Tata McGraw Hill, 2006, ISBN-10: 0073380490, ISBN-13: 978-0073380490.

Reference Book:

1. Oppenheim, Schafer: “Discrete Time Signal Processing”, 3rd Edition, Pearson Education, 2003, ISBN-10: 0131988425, ISBN-13: 978-0131988422.

E- Resources:

1. www.cmlab.csie.ntu.edu.tw/cml/dsp/training/coding/transform/fft.html
2. www.tech-faq.com/iir-filter.html
3. www.labbookpages.co.uk/audio/firWindowing.html
4. www.multirate-digital-signal-processing-pdf.html



Digital Communication

Course Code	L:T:P:S	Credits	Exam marks	Exam Duration	Course Type
17ECT62	4:0:0:0	4	CIE:50 SEE:50	3 Hours	FC

Course Objectives:

This course will enable students to :

- Understand the Nyquist Sampling Theorem.
- Understand the different quantization techniques.
- Understand the need for DPCM, DM and ADM.
- Analyze the concept of Detection and Estimation.
- Understand the Coherent modulation techniques such as BPSK, ASK, DPSK and QPSK systems.

Syllabus

Module – I

Signal Sampling: Basic signal processing operations in digital communication, Sampling Principles, Sampling Theorem, Quadrature sampling of band-pass signals, PAM, TDM. **10 Hours**

Module – II

Waveform Coding Techniques: PCM block diagram, Different quantization techniques, SNR in PCM, robust quantization, DPCM, DM, Adaptive DM. **10 Hours**

Module – III

Base-Band Shaping for Data Transmission: Line Codes and their power spectra, ISI, Nyquist criterion for distortion less base-band binary transmission, adaptive equalization, eye pattern. **10 Hours**

Module – IV

Digital Modulation and Demodulation Techniques: Coherent binary modulation techniques, BPSK, FSK, ASK, DPSK, QPSK systems with signal space diagram, generation, demodulation and error probability concept, Comparison using Power Spectrum, Coherent demodulation techniques for ASK, FSK and BPSK. **10 Hours**

Module – V

Spread Spectrum Modulation: Pseudo noise sequences, notion of spread spectrum, direct sequence spread spectrum, coherent binary PSK, frequency hop spread spectrum, applications. **10 Hours**

Course Outcomes:

On completion of this module, students will be able to :

- Sample a signal and reconstruct it at receiver.
- Design a PCM, DPCM, DM and ADM systems.
- Design Base Band shaping for data transmission.
- Describe system level blocks for BPSK, ASK, DPSK and QPSK systems.
- Analyze coherent and no-coherent digital modulation systems and understand the basics of spread spectrum technology.

Text Books:

1. Simon Haykin: “Digital Communications”, John Wiley, 2003.
2. J. Proakis: “Digital Communication”, 4th Edition, McGraw Hill, 2000.

Reference Books:

1. K. Sam Shanmugam: “Digital and Analog Communication Systems”, John Wiley, 1996.
2. Simon Haykin: “An Introduction to Analog and Digital Communication”, John Wiley, 2003.
3. Bernard Sklar: “Digital Communications”, Pearson Education, 2007.
4. K. Sam Shanmugam, A. M. Breipohl: “Random Signals: Detection, Estimation and Data Analysis”, Wiley, 1988.

E-Resources:

1. https://www.tutorialspoint.com/signals_and_systems/signals_sampling_theorem.htm
2. <http://www.electronicdesign.com/communications/understanding-modern-digital-modulation-techniques>
3. https://ned.ipac.caltech.edu/level5/Sept04/Wright/Wright6_2.html
4. https://www.tutorialspoint.com/digital_communication/digital_communication_quantization.htm
5. <http://nptel.ac.in/courses/117103018/44>



Antenna and Propagation (IC)

Course Code	L:T:P:S	Credits	Exam marks	Exam Duration	Course Type
17ECI631	3:0:2:0	4	CIE:50 SEE:50	3 Hours	FE

Course Objectives:

This course will enable students to :

- Understand the fundamental concepts of antenna parameters and definitions.
- Understand the different types of antenna arrays.
- Understand the basic knowledge of different types of antenna like Slot antennas, Yagi-Uda antennas, etc.
- Understand the importance of radio wave propagation and its effects.

Syllabus

Module - I

Basics of Antennas: Definition of antenna parameters – Gain, Directivity, Effective aperture, Radiation Resistance, Band width, Beam width, Input Impedance. Matching, Antenna noise temperature, Radiation from oscillating dipole, Half wave dipole. Folded dipole. **08 Hours**

Module – II

Aperture and slot antennas: Radiation from rectangular apertures, Uniform and Tapered aperture, Horn antenna, Reflector antenna, Aperture blockage, Feeding structures, Slot antennas, Microstrip antennas – Radiation mechanism – Applications, Numerical tool for antenna analysis. **08 Hours**

Module – III

Point source and Antenna arrays: N element linear array, Pattern multiplication, Broadside and End fire array – Concept of Phased arrays, Adaptive array, Basic principle of antenna Synthesis-Binomial array. **08 Hours**

Module – IV

Special antennas: Principle of frequency independent antennas - Spiral antenna, Helical antenna, Log periodic. Modern antennas- Reconfigurable antenna, Active antenna, Dielectric antennas, Electronic band gap structure and applications, Antenna Measurements-Test Ranges, Measurement of Gain, Radiation pattern, Polarization, VSWR, measurement of power by two-wattmeter method. Determination of power factor using wattmeter readings. Illustrative examples. **08 Hours**

Module – V

Propagation of Radio waves: Modes of propagation, Structure of atmosphere, Ground wave propagation, Tropospheric propagation, Duct propagation, Troposcatter propagation, Flat earth and Curved earth concept Sky wave propagation – Virtual height, Critical frequency, Maximum usable frequency – Skip distance, Fading, Multi-Hop propagation. **08 Hours**

List of experiments:

1. Performance analysis of Half wave dipole antenna.
2. Study of Folded dipole antenna.
3. Analysis of Loop antenna.
4. Calculation of Directivity and other parameters for Yagi-Uda antenna.
5. Performance study of Helix antenna.
6. Calculation of radio wave propagation path loss.

Course Outcomes:

On completion of this course, students will be able to :

- Describe the basic parameters of antenna.
- Discuss the concepts of aperture and slot antenna.
- Analyze antenna arrays.
- Understand the concept and principle of special antennas.
- Understand the propagation of radio waves.

Text Books:

1. William H Hayt Jr., John A Buck: "Engineering Electromagnetics", 7th Edition, Tata McGraw-Hill, 2006.
2. A. R. Harish, M. Sachidananda: "Antenna and Wave Propagation", Oxford University Press India, 2007.

Reference Books:

1. John Krauss, Daniel A Fleisch: "Electromagnetics with Applications", 5th Edition, McGraw-Hill, 1999.
2. Edward C. Jordan, Keith G Balmain: "Electromagnetic Waves and Radiating Systems", 2nd Edition, Prentice Hall of India / Pearson Education, 1968.

E-Resources:

1. <http://physics.usask.ca/~hirose/ep225/radiation.htm>
2. http://www.radio-electronics.com/info/antennas/horn_antenna/horn_antenna.php
3. http://gmrt.ncra.tifr.res.in/gmrt_hpage/Users/doc/WEBLF/LFRA/node43.html
4. <https://www.electronics-notes.com/articles/antennas-propagation/tropospheric/tropospheric-propagation.php>



Database Concepts (IC)

Course Code	L:T:P:S	Credits	Exam marks	Exam Duration	Course Type
17ECI632	3:0:2:0	4	CIE:50 SEE:50	3 Hours	FE

Course Objectives:

This course will enable the students to :

- Understand the basic concepts of database and Database Management System.
- Understand the difference between relational systems and non-relational systems.
- Get a clear understanding of how to maintain data (CRUD operations) in a relational database.
- Understand the working of a non-relational database.
- Get a clear understanding of how to maintain data (CRUD operations) in a non-relational database.
- Understand how Java programs can access database management systems using JDBC.

Syllabus

Module – I

Introduction to Database: Relational Database, Schemaless Database.

Introduction to SQL: DDL, DML, DQL, TCL, DCL.

Constraints: Not null, Unique, Primary key, foreign key.

08 Hours

Module – II

Operators and Functions: Operators and Functions.

Joins, Inner queries, Co-related queries: Joins, Inner queries, Co-related queries.

07 Hours

Module – III

JDBC: JDBC API, Statement / Prepared Statements / Callable Statments, Result Set, CRUD operations.

05 Hours

Module – IV

MongoDB: Introduction and Installation, CRUD operations, Projections.

06 Hours

Module – V

Project Work: To create a Banking application using the concepts of database management systems with special emphasis on Java, JDBC, and MySQL database.

14 Hours

Laboratory

1. Programs covering SQL, Constraints, Operators and Functions, Joins, Inner queries, Co-related queries.
2. Programs covering JDBC concepts.
3. Programs covering MongoDB concepts.

Course Outcomes

On completion of this course, students will be able to :

- Explain the basic concepts of database and Database Management System.
- Differentiate between relational systems and non-relational systems.
- Describe how to maintain data (CRUD operations) in relational and non-relational database.
- Manage Java programs to access database management systems using JDBC.
- Save and retrieve data in a safe and consistent manner.

Text Books:

1. Rajiv Chopra: “Database Management Systems (DBMS)”, 5th Edition, S Chand Publishing, ISBN-10: 9385676342.
2. Kristina Chodorow, Shroff: “MongoDB: The Definitive Guide”, 2nd Edition, ISBN-10: 9351102696.

Reference Books:

1. Raghu Ramakrishnan: “Database Management Systems (Asia Higher Education Engineering/Computer Science)”, 3rd Edition, McGraw Hill Education, ISBN-10: 007123151X.
2. Kyle Banker, Peter Bakkum, Shaun Verch: “MongoDB in Action: Covers MongoDB Version 3.0”, 2nd Edition, Dreamtech Press, ISBN-10: 9351199355.



Operations Research

Course Code	L:T:P:S	Credits	Exam marks	Exam Duration	Course Type
17ECT641	3:0:0:0	3	CIE:50 SEE:50	3 Hours	EE

Course Objectives:

This course will enable students to :

- Understand the how to use variables for formulating complex mathematical models in management science, industrial engineering and transportation science.
- Understand the use of basic methodology for the solution of linear programs and integer programs.

Syllabus

Module – I

Introduction: Linear programming, Definition, scope of Operations Research (O.R) approach and limitations of OR Models, Characteristics and phases of OR Mathematical formulation of L.P. Problems. **08 Hours**

Module – II

Linear Programming Problems: The simplex method - slack, surplus and artificial variables. Concept of duality, two phase method, dual simplex method, degeneracy, and procedure for resolving degenerate cases. **07 Hours**

Module – III

Transportation Problem: Formulation of transportation model, Basic feasible solution using different methods, Optimality Methods, Unbalanced transportation problem, Degeneracy in transportation problems, Applications of Transportation problems.

Assignment Problem: Formulation, unbalanced assignment problem. **08 Hours**

Module – IV

PERT-CPM Techniques: Network construction, determining critical path, floats, scheduling by network, project duration, variance under probabilistic models, prediction of date of completion, crashing of simple networks. **08 Hours**

Module – V

Integer Programming: Gomory's technique, branch and bound algorithm for integer programming problems, zero one algorithm. **08 Hours**

Course Outcomes:

On completion of this course, students will be able to :

- Describe some basics of Linear programming and solve problems on the same
- Analyze Linear programming problems
- Describe and Analyze Transportation problems
- Describe the various methods involved in CPM technique
- Understand the basics of Integer programming

Text Books:

1. Taha H. A.: "Operations Research and Introduction", Pearson Education.
2. S. D. Sharma: "Operations Research", Kedarnath Ramnath & Co., 2002.

Reference Book:

1. Harold Koontz, Heinz Weihrich: "Essentials of Management", 7th Edition.

E-Resources:

1. http://www.phpsimplex.com/en/simplex_method_example.htm
2. <https://ocw.mit.edu/courses/sloan-school-of-management/15-053-optimization-methods-in-management-science-spring-2013/tutorials/>
3. <http://www.dspguide.com/ch10/2.htm>



Robotics

Course Code	L:T:P:S	Credits	Exam marks	Exam Duration	Course Type
17ECT642	3:0:0:0	3	CIE:50 SEE:50	3 Hours	EE

Course Objectives:

This course will enable students to :

- Understand the automation and brief history of robot and applications.
- Understand the Robot end effectors and their design.
- Understand the Robot Programming methods & Languages of robot.
- Understand the Various Sensors and their applications in robots.

Syllabus

Module – I

Introduction: Introduction -- brief history, types, classification and usage, Science and Technology of robots. **07 Hours**

Module – II

Elements of robots – links, joints, actuators, and sensors: Position and orientation of a rigid body, Homogeneous transformations, Representation of joints, link representation using D-H parameters, Examples of D-H parameters and link transforms, different kinds of actuators – stepper, DC servo and brushless motors, model of a DC servo motor, Types of transmissions, Purpose of sensors, internal and external sensors, common sensors – encoders, tachometers, strain gauge based force-torque sensors, proximity and distance measuring sensors, and vision. **08 Hours**

Module – III

Kinematics of serial robots: Introduction, Direct and inverse kinematics problems, Examples of kinematics of common serial manipulators, workspace of a serial robot, Inverse kinematics of constrained and redundant robots, Tractrix based approach for fixed and free robots and multi-body systems, simulations and experiments, Solution procedures using theory of elimination, Inverse kinematics solution for the general 6R serial manipulator. **08 Hours**

Module – IV

Kinematics of parallel robots: Degrees-of- freedom of parallel mechanisms and manipulators, Active and passive joints, Constraint and loop-closure equations, Direct kinematics problem, Mobility of parallel manipulators, Closed-form and numerical solution, Inverse kinematics of parallel manipulators and mechanisms, Direct kinematics of Gough-Stewart platform. **08 Hours**

Module – V

Velocity and static analysis of robot manipulators: Linear and angular velocity of links, Velocity propagation, Manipulator Jacobians for serial and parallel manipulators, Velocity ellipse and ellipsoids, Singularity analysis for serial and parallel manipulators, Loss and gain of degree of freedom, Statics of serial and parallel manipulators, Statics and force transformation matrix of a Gough Stewart platform, Singularity analysis and statics. **08 Hours**

Course Outcomes:

On completion of this course, students will be able to :

- Understand the basics of automation and also brief history of robot and applications.
- Familiarize with the kinematic motions of robot.
- Have good knowledge about robot end effectors and their design concepts.
- Analyze with the Programming methods & various Languages of robots.
- Familiarize with the principles of various Sensors and their applications in robots.

Text Book:

1. Ghosal A.: “Robotics: Fundamental Concepts and Analysis”, 2nd Edition, Oxford University Press, 2008, ISBN-10: 0195673913 , ISBN-13: 978-0195673913.

Reference Book:

1. Fu K., Gonzalez, R., Lee, C. S. G.: “Robotics: Control, Sensing, Vision and Intelligence”, Tata McGraw Hill, 1987, ISBN-10: 0070226253, ISBN-13: 9780070226258.

E-Resources:

1. <http://nptel.ac.in/courses/112108093/3>
2. <https://www.coursehero.com/file/p1kbmho/II-Elements-of-robots-joints-links-actuators-and-sensors-Position-and/>
3. <http://www8.tfe.umu.se/courses/elektro/RobotControl/>
4. <http://data.mecheng.adelaide.edu.au/avc/publications/thesis/>



Internet of Things (IoT) (IC)

Course Code	L:T:P:S	Credits	Exam marks	Exam Duration	Course Type
17ECI643	3:0:2:0	4	CIE:50 SEE:50	3 Hours	EE

Course objectives:

This course will enable students to :

- Understand the Vision and Introduction to IoT.
- Understand IoT Market perspective.
- Understand the data and knowledge management and use of devices in IoT technology.
- Understand State of the Art – IoT Architecture.
- Understand the Real World IoT Design Constraints, Industrial Automation and Commercial Building Automation in IoT.

Syllabus

Module – I

IoT and Web Technology: The Internet of Things Today, Time for Convergence, Towards the IoT Universe, Internet of Things Vision, IoT Strategic Research and Innovation Directions, IoT Applications, Future Internet Technologies, Infrastructure, Networks and Communication, Processes, Data Management, Security, Privacy & Trust, Device Level Energy Issues, IoT Related Standardization, Recommendations on Research Topics. **08 Hours**

Module – II

M2M to IoT – A Basic Perspective: Introduction, Some Definitions, M2M Value Chains, IoT Value Chains, An emerging industrial structure for IoT, The international driven global value chain and global information monopolies. **06 Hours**

Module – III

M2M to IoT-An Architectural Overview: Building an architecture, Main design principles and needed capabilities, An IoT architecture outline, standards considerations. **06 Hours**

Module – IV

IoT Architecture - State of the Art: Introduction, State of the art. Architecture Reference Model: Introduction, Reference Model and architecture, IoT reference Model.

IoT Reference Architecture: Introduction, Functional View, Information View, Deployment and Operational View, Other Relevant architectural views. **10 Hours**

Module – V

IoT Applications for Value Creations: Introduction, IoT applications for industry: Future Factory Concepts, Brownfield IoT, Smart Objects, Smart Applications, Four Aspects in your Business to Master IoT, Value Creation from Big Data and Serialization, IoT for Retailing Industry, IoT For Oil and Gas Industry, Opinions on IoT Application and Value for Industry, Home Management, eHealth. **10 Hours**

Laboratory

1. Sketch the architecture of IoT Toolkit and explain each entity in brief.
2. Demonstrate a smart object API gateway service reference implementation in IoT toolkit.
3. Write and explain working of an HTTP- to-CoAP semantic mapping proxy in IoT toolkit.
4. Explain application framework and embedded software agents for IoT toolkit.
5. Explain working of Raspberry Pi.
6. Connect Raspberry Pi with your existing system components.
7. Give overview of Zetta.

Design based Problems (DP) / Open Ended Problem:

1. How do you connect and display your Raspberry Pi on a Monitor Or TV?
2. Create any circuitry project using Arduino.

Major Equipment:

1. Raspberry pi, Arduino

Course Outcomes:

On completion of this course, students will be able to :

- Understand the vision of IoT from a global context.
- Determine the Market perspective of IoT.
- Use of Devices, Gateways and Data Management in IoT.
- Understand the building state of the art architecture in IoT.
- Application of IoT in Industrial and Commercial Building Automation and Real World Design Constraints.

Text Book:

1. Jan Holler, Vlasios Tsiatsis, Catherine Mulligan, Stefan Avesand, Stamatis Karnouskos, David Boyle: “From Machine-to-Machine to the Internet of Things: Introduction to a New Age of Intelligence”, 1st Edition, (Chapters 1-4,8-10), Academic Press, 2014, ISBN: 978080994017.

Reference Books:

1. Vijay Madiseti and Arshdeep Bahga: “Internet of Things (A Hands-on-Approach)”, 1st Edition, VPT, 2014.
2. Francis daCosta: “Rethinking the Internet of Things: A Scalable Approach to Connecting Everything”, 1st Edition, Apress Publications, 2013.

E-Resources:

- <https://github.com/connectIOT/iottoolkit>
- <https://www.arduino.cc/>
- <http://www.zettajs.org/>
- Contiki (Open source IoT operating system)
- Arduino (open source IoT project)



Digital Signal Processing Lab

Course Code	L:T:P:S	Credits	Exam marks	Exam Duration	Course Type
17ECL65	1:0:2:0	2	CIE:50 SEE:50	3 Hours	FC

Course Objectives:

This course will enable students to :

- Understand Working knowledge of the design & implementation on various DSP operations using MATLAB.
- Understand Practical perspective of convolution and filtering operations using DSP processor.

List of Experiments:

1. Verification of sampling theorem.
2. Linear Convolution of given two sequences.
3. Impulse response of a given system.
4. Circular convolution of given two sequences.
5. Autocorrelation of a given sequence and verification of its properties.
6. Cross correlation of given two sequences and verification of its properties.
7. Solving a given difference equation.
8. Computation of N point DFT of a given sequence and to plot magnitude and phase spectrum.
9. Linear convolution of two sequences using DFT and IDFT.
10. Circular convolution of given two sequences using DFT and IDFT.
11. Design and implementation of FIR filter to meet given specifications.
12. Design and implementation of IIR filter to meet given specifications.

B. List of Experiments using DSP Processor

1. Linear convolution of given two sequences.
2. Circular convolution of given two sequences.
3. Computation of N- Point DFT of a given sequence
4. Realization of an FIR filter (any type) to meet given specifications. The input can be a signal from function generator / speech signal.

5. Audio applications such as to plot time and frequency (Spectrum) display of Microphone output plus a cosine using DSP. Read a wav file and match with their respective spectrograms.
6. Noise: Add noise above 3kHz and then remove; Interference suppression using 400 Hz tone.
7. Impulse response of first order and second order system.

Course Outcomes

On completion of this course, students will be able to :

- Implement circular and section convolution.
- Implement linear convolution and circular convolution using DFT and IDFT.
- Implement digital FIR filter to meet the given specifications.
- Implement digital IIR filters to meet the given specification.
- Implement convolution and filtering using DSP processor.



LabVIEW - Level I

Course Code	L:T:P:S	Credits	Exam marks	Exam Duration	Course Type
17HOE661	2:0:0:4	3	CIE:50 SEE:50	3 Hours	OE

Course Objectives:

This course will enable students to :

- Understand the fundamental of graphical coding system.
- Learn to develop basic level of LabVIEW coding.
- Study the different component of LabVIEW operating tools.
- Study and develop state machine for a specific problem.
- Develop integrated coding solution for analysis and presentation with MyRio hardware using accelerometer.

Syllabus

Module - I

LabVIEW programming concepts, environment and Software constructs: Data flow, Polymorphism, Front panel window, block diagram, and connector pane, Menus and palettes, Configuration options. Controls, indicators, IO controls, and refnums Terminals, constants, nodes, update modes, and legends of charts and graphs. Mechanical action of Boolean objects Property Nodes. Numeric, string, Boolean, and path data types. Array and cluster data types. Shift registers, Case, Sequence and Event structures. **10 Hours**

Module - II

Programming, Data communication and synchronization VIs and functions: Conversion, comparison, and manipulation, Timing and Timing functions related to Timed structures. Data storage and file I/O formats, Waveform and waveform file I/O, Dynamic and User events Local, global, and shared variables Data Socket TCP and UDP Notifiers Queues Semaphores Property Nodes, and Invoke Nodes. **08 Hours**

Module - III

Error handling VIs and functions: Error clusters Dialog and User Interface VIs Custom error codes.

Design patterns: Simple state machine, User interface event handler, Queued message handler, producer/consumer (data) and producer/consumer (events), Functional global variables. **06 Hours**

Module - IV

Sub VI design: SubVI creation methods, Connector panes and connection types, Polymorphic subVIs, Options related

Debugging tools and techniques: Debugging tools, Error list window, Execution highlighting, Breakpoints and single stepping, Generic and custom probes, Debugging practices and techniques for different situations. **08 Hours**

Module - V

VI design and documentation (style) practices: Refer to the LabVIEW Style Checklist top of the LabVIEW Help for information on the following items

- i. User interface design and block diagram layout
- ii. Modular and hierarchical design
- iii. SubVI icons and connector pane layout (standard)
- iv. Properties
- v. Documenting Vis

Memory, performance, and determination

- a. Tools for identifying memory and performance issues
Profile memory and performance, Show buffer allocations and VI metrics
- b. Programming practices

Enforcing dataflow, User interface updates and response to user interface controls, Data type selection, coercion, and buffer allocation, Array, string, and loop operations **08 Hours**

Course Outcomes:

On completion of this course, students will be able to :

- Formulate basic aspects of the graphical programming using LabVIEW 2016.
- Develop LabVIEW coding for a specific problem of datalogging, measurement and presentation.
- Handle the error function and errors in the LabVIEW coding.
- Develop coding for data handling and Analysis on the acquired data.
- Design a state machine LabVIEW coding for an applied problem.

Text Books:

1. "LabVIEW - Getting Started with LabVIEW", M/s National Instruments, 2013

373427J-01.

2. Jovitha Jerome: "Virtual instrumentation using labview", PHI Learning Pvt. Ltd., 2010.
3. Hans-Petter Halvorsen: "Introduction to LabVIEW," University College of Southeast, Norway.
4. S. Sumathi, P. Surekha: "LabVIEW based Advanced Instrumentation Systems", Springer.
5. Lab manual provided by Dept. of Civil Engg., NCET.

Reference Books:

1. Jeffrey Travis, Jim Kring: "Introduction to Graphical Programming with LabVIEW", Pearson, 2006.
2. Malan Shiralkar: "LabVIEW Graphical Programming Course Collection", National Instruments.

E-Resources:

1. <http://cnx.org/content/col10241/1.4>.



Yoga and Meditation

Course Code	L:T:P:S	Credits	Exam marks	Exam Duration	Course Type
17HOE662	2:0:0:4	3	CIE:50 SEE:50	3 Hours	OE

Course Objectives:

This course will enable students to :

- Introduce the main principles of Yoga.
- Generate knowledge and skills of students to use the tools and techniques for using Yoga in day to day life for better health and well being.
- Improve communication and increase concentration through Yoga and Meditation.
- Equip the individual to handle stressful situations and manage day to day activities.

Syllabus

Module – I

Definition and meaning of yoga: Meaning of Asanas, Types of Asanas: standing, sitting and supine asanas. Standing Asanas (Trikon asan, padhastasan, ardhachakrasan, veerbhadrasan), Sitting Asanas (Vajrasan, padmasan, suptavajrasan, Ardhamaschendrasan, vakrasan), Supine Asanas (Sarvangasan, Matsyasan, Natarajasan, Shavasana). **08 Hours**

Module – II

Patanjali's Yoga Sutra: Eight limbs of yoga, Importance of discipline in Yoga, Stillness of mind, Five Modulations (vritti) of the mind, Practice and Dispassion, Obstacles in the path of Yoga, Overcoming distractions of the mind through Yoga. **08 Hours**

Module – III

Understanding physiological implications of Yoga, Three types of Gunas (Satva, Rajas and Tamas) and their effects on body and mind, Food Habits, Meaning of Prana, Pranayama and its advantages, Different types of Pranayama. **08 Hours**

Module – IV

Ayurveda: The science of life, Three types of doshas (Vata, Pitta and Kapha), Balancing the different doshas for a healthy life, Ayurvedic principles of food and activity, Advanced Asanas: Mayurasana, Sirsasana, Gomukh Asana, Vrksasana, Baddha Konasana. **08 Hours**

Module – V

Meditation: Meaning of meditation, Meditation vs Concentration, Advantages of Meditation, Effects of Meditation on body and mind, Effect on health and general well being, Reducing stress through meditation, Increasing concentration, Improving communication, Effect on Environment. **08 Hours**

Course Outcomes:

On completion of this course, students will be able to :

- Know the basic principles of Yoga.
- Know and practice the basic asanas and their benefits.
- Use Pranayama and Meditation for improving health and mental peace.
- Know the difference between meditation and concentration.
- Apply the principles of Ayurveda and implement them for one's benefit.

Text Books:

1. Yoga Sutras of Patanjali (ancient text).
2. B K S Iyengar: "Light on Yoga".

Reference Books:

1. A traditional touch to Yogasanas for beginners and Sadhakas, Swami Vivekananda Yoga Prakashana (SVYP).
2. Dr. Vasant Lad: "Ayurveda: The Science of Self-Healing: A Practical Guide".



Martial Arts

Course Code	L:T:P:S	Credits	Exam marks	Exam Duration	Course Type
17HOE663	2:0:0:4	3	CIE:50 SEE:50	3 Hours	OE

The following types of Martial arts are offered,

1. Karate
2. Taekwondo
3. Judo
4. Kung-fu

Expert Trainers will be provided during the academic year through experts in Martial Arts. Students who enroll for this elective should attend the regular Training classes and maintain a minimum of 85% attendance.

At the end of the training programme the performance Evaluation will be made by team of experts. Students who secure at least a satisfactory grade will be issued a certificate and deemed to have been completed the above said 3 Credit course. However, the students who have shortage of attendance will be consider for the award of 3 credits provided they undergo training at any of the training centers in the above said Martial Arts, complete the certification programme and give a demo along with viva in the presence of experts in the campus.



Music (Carnatic Vocal/Instrumental)

Course Code	L:T:P:S	Credits	Exam marks	Exam Duration	Course Type
17HOE664	2:0:0:4	3	CIE:50 SEE:50	3 Hours	OE

Course Objectives:

This course will enable students to :

- Get familiarized with the conceptual understanding of Carnatic music.
- Gain knowledge about the basics of Swaravalis.
- Understand the use of different Talas.
- Gain understanding about various Raagas.
- Gain understanding about intricacies of Swaras.

Syllabus

Module – I

Theoretical Aspects: Father of Carnatic music, Famous personalities in Carnatic music, Concept of Sapta Swara, Taala, Melody, Pitch, Rhythm, Janaka Raaga, Janya Raaga. **03 Hours**

Module – II

Sarale Varase (Any 5), Janti Varase (Any 5), Daatu Varase, Tara Stayi, Mandra Stayi. **08 Hours**

Module – III

Alankaras: Druva Taala, Matya Taala, Tripura Taala, Rupaka Taala, Jampe Taala, Atta Taala, Eka Taala. **08 Hours**

Module – IV

Geethagalu, Pillari Geethe (4), Sanchari Geethe (5), Lakshana Geethe (1). **10 Hours**

Module – V

Swarajatis (Any 2), Kalyani, Bilahari, Neelambari, Kamach. Varna (Any 2), Shankarabara, Kalyani, Hamsadwani, Mohana. **10 Hours**

Course Outcomes:

On completion of this course, students will be able to :

- Gain knowledge about the theoretical background of Carnatic music
- Acquire practical knowledge on basics of Carnatic music.
- Practical demonstration of different Talas.
- Distinguish among various Raagas based on swara sthanas.
- To synchronize the Raaga and Taala.

Text books:

1. Dr. Sachidevi: "Karnataka Sangeetha Darpana", Sreenivasa Prakashana, Bengaluru, 2014.
2. Junior Carnatic Music – C Shiva Musicals, Malleshwaram, Bengaluru, 2013.

Dance (Bharatanaty)

Course Code	L:T:P:S	Credits	Exam marks	Exam Duration	Course Type
17HOE665	2:0:0:4	3	CIE:50 SEE:50	3 Hours	OE

Course Objectives:

This course will enable students to :

- Gain theoretical knowledge about various types of Indian dances.
- Understand about various musical instruments used in Bharatanaty.
- Learn Practical demonstrations of Bharatanaty steps on Prarthane Namaskara and Shlokas.
- Learn the movements of head, neck, eyes, hands according to Bharatanaty steps.
- Learn the brisk movements in Bharatanaty with the help of ADAVUS.

Syllabus

Module – I

Indian Classical dance, It's history and Significance, Types of Classical Dance, Bharatanaty, Kathakali, Mohini Attam, Koochipudi, Katahak, Odissi, Manipuri.

04 Hours

Module – II

Musical Instruments used in Bharatanaty: Tabala, Mrudanga, Kamsale, Kolata, Taala vadya. Famous personalities in Bharatanaty, Composers of Natya Grantas.

03 Hours

Module – III

Practical exercises on Prarthane, Namaskara and Shloka, Vyayama Kriye for Bharatanaty (Two Shlokas and Two Prarthanes).

10 Hours

Module – IV

Abhinaya Steps (Chaturvidha) ShiroBedha, Drushti Bedha, Greeva Bedha, Brubedha, Hasta Bedha (Samyuta and Asamyuta).

10 Hours

Module – V

Adavugalu (DashaVidha) Tattu adavu, Mettu Adavu, Nat Adavu, Egaru Tattu Adavu, Egaru Mettu adavu, Jaaru Adavu, Mandi adavu, TattuMettu Adavu, Rangakarma Adavu, Teermana Adavu.

12 Hours

Course Outcomes:

On completion of this course, students will be able to :

- Get an insight into various types of Indian dances.
- Gain knowledge of different instruments used to perform dance.
- Perform exercises on prarthane, Namaskara according to Bharatanaty style.
- Perform basic steps in Abhinaya.
- Recognise and perform different Adavus.

Reference Book:

1. "Bharatanaty shastra", Department of Public Instruction, Karnataka State Government.

Sports

Course Code	L:T:P:S	Credits	Exam marks	Exam Duration	Course Type
17HOE666	2:0:0:4	3	CIE:50 SEE:50	3 Hours	OE

Students who are selected by the University to represent the VTU teams and for participating at State level / National level Sports in the following sports are exempted from taking open elective (Code:17HOE666) and will be awarded 3 credits.

Outdoor games	Indoor games
Cricket	Carrom
Foot ball	Chess
Hockey	Shuttle Badminton
Basket Ball	Squash
Kabbadi	Table – Tennis
Kho – Kho	Gymnastics
Hand – Ball	
Athletics	
Swimming	
Lawn Tennis	

The achievement in Sports as said above should have been made during the academic year during which the said open elective is offered.

After representing at VTU / State / National level in any of the above said sports, the students should produce the certificates from the competent authorities. Based on the certificates the institution will issue another certificate related to the achievement and awarding of three credits.



Digital Communication Lab

Course Code	L:T:P:S	Credits	Exam marks	Exam Duration	Course Type
17ECL67	1:0:2:0	2	CIE:50 SEE:50	3 Hours	FC

Course Objectives:

This course will enable students to :

- A practical knowledge of different digital modulation techniques.
- The implementation of design concepts with software.

List of Experiments:

Hardware

1. TDM of two band limited signals.
2. ASK generation and detection.
3. FSK generation and detection.
4. PSK generation and detection.
5. QPSK generation.
6. Analog and Digital (with TDM) communication link using optical fiber.
7. Verification of sampling theorem using Flat-top sampling.

Software

1. ASK modulation and demodulation.
2. FSK modulation and demodulation.
3. PSK modulation and demodulation.
4. QPSK modulation and demodulation.

Course Outcomes:

On completion of this course, students will be able to :

- Implement ASK, PSK and FSK.
- Implement TDM using optical fiber.
- Demonstrate the QPSK generation.
- Realize the design theory concept using software.
- Analyze and understand the outputs by changing the important parameters.



Technical Aptitude and GD

Course Code	L:T:P:S	Credits	Exam marks	Exam Duration	Course Type
15CSH68	2:0:0:0	2	CIE:50 SEE:50	3 Hours	HSS

The respective branches shall conduct training programmes related to important and latest programming languages and other emerging technologies, such as Solar and Electric power based gadgets, IoT, ROBOT's, Environmental friendly and cost effective construction techniques, UAV's and technologies pertaining to the respective department.



Seventh Semester B.E. – Syllabus

Power Electronics

Course Code	L:T:P:S	Credits	Exam marks	Exam Duration	Course Type
17ECT71	3:0:0:0	3	CIE:50 SEE:50	3 Hours	FC

Course Objectives:

This course will enable students to :

- Understand the meaning and importance of power electronics.
- Understand the main switching topologies used in power electronics circuits and how they operate, how they are controlled, driven and protected.
- Understand the principle of operation of a thyristor.
- Understand the different configurations of control rectifiers.
- Understand the categorize different commutation techniques.
- Understand the categorize AC voltage controllers.
- Understand the conceptualize DC-DC converters.
- Understand the principles of inverters.

Syllabus

Module – I

Power Devices: Application of power electronics, Power BJT's, Switching characteristics, Switching units, Base drive control, Power MOSFETs, Switching characteristics, Gate drives, IGBTs, Isolation of gate and base drive, Construction of thyristor, Principle of operation, Different states/Modes of operation, Static anode VI characteristics, Two transistor model, Triggering/Turn-on mechanism, Dynamic (Turn-on and Turn-off), Characteristics, Gate characteristics, Gate triggering, di/dt and dv/dt protection, Thyristor firing circuits. **08 Hours**

Module – II

Control Rectifier: Introduction, Principle of phase controlled converter operation, Single phase half controlled converter, Single phase fully controlled converter, Dual converter, Three phase half controlled converter, Three phase fully controlled converter. **08 Hours**

Module – III

Commutation Techniques: Introduction to commutation, Different types of commutations, Natural commutation and forced commutation, Self-commutation, Complementary commutation, Auxiliary thyristor commutation. **08 Hours**

Module – IV

AC Voltage Controllers and Choppers: Introduction to choppers, Principles of step down and step up choppers, Step down chopper with RL load, Classification of chopper, Analysis of impulse commutated thyristor chopper, Introduction to AC voltage controllers, Principle of ON-Off control, Principle of phase control, Single-phase AC controllers with R load and RL load. **08 Hours**

Module – V

Inverters: Introduction, Principle of operation, Performance parameters, Single-phase bridge inverter, Voltage control of single-phase inverters, Current source inverters. **08 Hours**

Course Outcomes:

On completion of this module, students will be able to :

- Design drive controls for power semiconductor devices.
- Analyze the operation of single phase and three phase rectifiers with various loads.
- Design commutation circuits.
- Design AC-voltage controllers for different configurations.
- Analyze the operation of choppers and inverters.

Text Books:

1. M. H. Rashid: “Power Electronics Circuits, Devices and Applications”, 3rd Edition, Pearson India, New Delhi, 2014, ISBN: 978-9332518445
2. G. K. Dubey, S. R. Doradla, A. Joshi, R. M. K. Sinha: “Thyristorized Power Controllers”, 6th Edition, New Age International Pvt. Ltd., 1986, ISBN: 9788122434224.

Reference Book:

1. P. S. Bhimbra: “Power Electronics”, Khanna Publication, 1995, ISBN: 9788174092-793.

E-Resources:

1. <https://www.electrical4u.com/power-mosfet/>
2. <http://www.completepowerelectronics.com/power-mosfet/>
3. <http://www.electronics-tutorial.net/dc-dc-converter-chopper/step-down-chopper/>
4. <http://www.electronics-tutorial.net/dc-to-ac-inverter/single-phase-full-bridge-inverter/>
5. <http://www.electricalidea.com/2016/05/21/single-phase-inverter/>

Data Communication

Course Code	L:T:P:S	Credits	Exam marks	Exam Duration	Course Type
17ECT72	3:0:0:0	3	CIE:50 SEE:50	3 Hours	FC

Course Objectives:

This course will enable students to :

- Understand the fundamental of layered task and OSI model and its functions.
- Understand the concepts of Data link control and its protocols.
- Understand the concepts of Wired LAN and Ethernet standards.
- Understand the concepts of Virtual LANs and connecting devices.
- Understand the application of Network layer.

Syllabus

Module – I

Layered tasks, OSI Model, Layers in OSI model, TCP/IP Suite, Addressing, Telephone and cable networks for data transmission, Telephone networks, Dial up modem, DSL, Cable TV for data transmission. **08 Hours**

Module – II

Data Link Control: Framing, Flow and error control, Protocols, Noiseless channels and noisy channels, HDLC. **08 Hours**

Module – III

Wired LAN, Ethernet, IEEE standards, Standard Ethernet, changes in the standards, Fast Ethernet, Gigabit Ethernet. **08 Hours**

Module – IV

Connecting LANs, Backbone and Virtual LANs, Connecting devices, Back bone Networks, Virtual LANs. **07 Hours**

Module – V

Network Layer, Logical addressing, Ipv4 addresses, Ipv6 addresses, Ipv4 and Ipv6 Transition from Ipv4 to Ipv6 **08 Hours**

Course Outcomes:

On completion of this module, students will be able to :

- Describe layers of OSI model and its functions.
- Discuss the different protocols of Noiseless and Noisy channels.
- Analyze different types of Ethernet.
- Distinguish between Virtual and Connecting LANs.
- Describe the functions of network layer, Transition from Ipv4 to Ipv6.

Text Book:

1. B Forouzan: “Data Communication and Networking”, 4th Edition, TMH, 2006.

Reference Books:

1. , James F. Kurose, Keith W. Ross: “Computer Networks”, Pearson education, 2nd Edition, 2003.
2. Wayne Tomasi: “Introduction to Data communication and Networking”, Pearson Education, 2007.

E-Resources:

1. http://www.webopedia.com/quick_ref/OSI_Layers.asp
2. <https://gradeup.co/flow-and-error-control-techniques-i-28750a29-ba8d-11e5-b537-dcac2f2dd7d1>
3. http://www.cse.wustl.edu/~jain/cis788-97/ftp/virtual_lans/
4. <http://www.studytonight.com/computer-networks/osi-model-network-layer>
5. https://www.tutorialspoint.com/data_communication_computer_network/network_layer_introduction.htm



Optical Fiber Communication (IC)

Course Code	L:T:P:S	Credits	Exam marks	Exam Duration	Course Type
17ECI731	3:0:2:0	4	CIE:50 SEE:50	3 Hours	FE

Course Objectives:

This course will enable students to :

- Understand the fundamental concepts in Optical Fiber Communication.
- Understand the types and function of various sources and detectors used OfC.
- Understand the basic knowledge of Fiber Couplers, Fiber Connectors and Optical Receivers.
- Understand the characteristics of Analog and Digital links.
- Understand the concept of WDM and Optical Networks.

Syllabus

Module – I

Overview of optical fiber communication: Introduction, Historical development, general system, advantages, disadvantages, and applications of optical fiber communication, optical fiber waveguides, Ray theory, cylindrical fiber, single mode fiber, cutoff wave length, mode field diameter. Optical Fibers: fiber materials, photonic crystal, fiber optic cables specialty fibers. **08 Hours**

Module – II

Transmission Characteristics of Optical Fibers: Introduction, Attenuation, absorption, scattering losses, bending loss, dispersion, Intra model dispersion, Inter model dispersion.

Optical Sources and Detectors: Introduction, LED's, LASER diodes, Photo detectors, Photo detector noise, Response time, double heterojunction structure, Photo diodes, comparison of photo detectors. **08 Hours**

Module – III

Fiber couplers and connectors: Introduction, fiber alignment and joint loss, single mode fiber joints, fiber splices, fiber connectors and fiber couplers.

Optical receiver: Introduction, Optical Receiver Operation, receiver sensitivity, quantum limit, eye diagrams, coherent detection, burst mode receiver, operation, Analog receivers. **08 Hours**

Module – IV

Analog and digital links: Analog links – Introduction, overview of analog links, CNR, multichannel transmission techniques, RF over fiber, key link parameters, Radio over fiber links, microwave photonics.

Digital links – Introduction, point-to-point links, System considerations, link power budget, resistive budget, short wave length band, transmission distance for single mode fibers, Power penalties, nodal noise and chirping. **08 Hours**

Module – V

WDM concepts and components: WDM concepts, overview of WDM operation principles, WDM standards, multiplexer, Isolators and circulators, direct thin film filters, active optical components, MEMS technology, variable optical attenuators, tunable optical fibers, polarization controllers, chromatic dispersion compensators, tunable light sources.

Optical Amplifiers and Networks – optical amplifiers, basic applications and types, semiconductor optical amplifiers, EDFA.

Optical Networks: Introduction, SONET / SDH, Optical Interfaces, SONET/SDH rings, High – speed light – waveguides. **08 Hours**

List of Experiments:

1. Demonstration and study of different types of Optical Fibers and connectors.
2. To establish and Study of a 650nm fiber optic analog link.
3. To establish and Study of a 650nm fiber optic digital link.
4. Study of Intensity Modulation Technique using Analog input signal. To obtain intensity modulation of the analog signal, transmit it over a fiber optic cable and demodulate the same at the receiver and to get back the original signal.
5. Study of Intensity Modulation Technique using digital Input signal. The objective of this experiment is to obtain intensity modulation of digital signal, transmit it over fiber optic cable and demodulate the same at the receiver end to get back the original signal.
6. To measure propagation or attenuation loss in optical fiber.
7. To measure propagation loss in optical fiber using optical power meter.
8. To measurement of the Numerical Aperture (NA) of the fibre.

Course Outcomes:

On completion of this course, students will be able to :

- Know the fundamental concepts of OfC.
- Analyze the characteristics of Optical Fiber and functions of various Sources and Detectors.
- Describe various Fiber couplers, connectors and analyse Optical receivers.
- Distinguish Analog and Digital Links.
- Discuss concept of WDM, Optical Amplifiers and Optical networks.

Text Books:

1. Gerd Keiser: "Optical Fiber Communication", MGH, 4th Edition, 2008.
2. John M. Senior: "Optical Fiber Communications", Pearson Education, 3rd Impression, 2007.

Reference Book:

1. Joseph C Palais: "Fiber Optic Communication", 4th Edition, Pearson Education.

E-Resources:

1. vlab.ambita.edu/index.php
2. <http://nptel.ac.in/syllabus/syllabus.php?subjectId=108105053>
3. <http://nptel.ac.in/syllabus/syllabus.php?subjectId=108108076>
4. <http://nptel.ac.in/courses/108108076/>



Web Technologies – Servlet, JSP (IC)

Course Code	L:T:P:S	Credits	Exam marks	Exam Duration	Course Type
17ECI732	3:0:2:0	4	CIE:50 SEE:50	n/a	FE

Course Objectives:

This course will enable students to :

- Understand the concepts of Web Technologies.
- Understand what are Web Servers and App Servers, and their differences.
- Understand Request and Response models.
- Understand how to build e-commerce applications using Servlets and JSP.
- Understand what EL and EL Tags are, and their usage in developing dynamic web pages.

Syllabus

Module – I

Introduction to Web Technologies: JEE, PHP, ASP and .Net

Introduction Web Dev environment: App Server, Web Server, 2-Tier and 3 -Tier Architecture.

Introduction to Servlet: Introduction to JEE containers, Application directory structure, Servlet Interface / Generic Servlet / HttpServlet, Servlet life cycle, Request and Response objects, Building sample application. **08 Hours**

Module – II

Inter Servlet Communication: RequestDispatcher, Include / Forward / Redirect, Building sample application.

Session Management: Creating & invalidating session, Different ways to handle session, Session time out configuration. **08 Hours**

Module – III

Introduction to JSP: Need for JSP, JSP life cycle. **06 Hours**

Module – IV

Introduction to EL: Need for EL and its advantages, Fundamentals of EL.

EL Tags: Core Tags, Introduction to MVC, Building sample application. **08 Hours**

Module – V

Project Work: Create an e-commerce application using the client-side languages, such as Bootstrap3, HTML5, CSS3, JavaScript and jQuery, along with the server-side Java language - Servlets and JSP. **10 Hours**

Laboratory

1. Programs covering Web Technologies, Web Dev environment, Servlet.
2. Programs covering Inter Servlet Communication, Session Management.
3. Programs covering JSP, Introduction to EL, EL Tags.

Course Outcomes:

On completion of this course, the students are able to :

- Analyze the concepts of Web Technologies.
- Compare Web Servers and App Servers.
- Implement Request and Response models.
- Demonstrate how to build e-commerce applications using Servlets and JSP.
- Design dynamic web pages using EL Tags.

Text Books:

1. Basham, Bryan, Sierra Kathy, Bates, Bert: “Head First Servlets and JSP”, 2nd Edition, Shroff, ISBN-10: 8184044976.
2. Santosh Kumar K: “JDBC 4.2, Servlet 3.1, and JSP 2.3 includes JSF 2.2 and Design Patterns, Black Book”, 2nd Edition, Dreamtech Press, ISBN-10: 9351199088.

Reference Book:

1. Budi Kurniawan: “Servlet & JSP: A Tutorial”, 2nd Edition, Brainy Software, ISBN-10: 1771970278.



Wireless Communication

Course Code	L:T:P:S	Credits	Exam marks	Exam Duration	Course Type
17ECT741	3:0:0:0	3	CIE:50 SEE:50	3 Hours	EE

Course Objectives:

This course will enable students to :

- The basic concepts behind wireless communication system
- The concept of path losses in radio wave propagation
- The different types of modulation techniques used in radio communication

Syllabus

Module – I

Introduction to wireless communication systems: Evolution of mobile radio commutation, mobile radio telephonic, Mobile radio systems around the world, Examples of wireless communication systems, paging systems, cordless telephone systems, cellular telephone systems, comparisons of common wireless communication systems, trends in cellular radio and personal communication systems. **08 Hours**

Module – II

The cellular concept –System Design Fundamentals: Introduction, Frequency Reuse, Channel Assignment strategies, Handoff strategies, Interference and system capacity, Trunking and Grade of service, improving coverage and capacity in cellular systems. **08 Hours**

Module – III

Mobile radio propagation, Large scale path loss: Introduction to radio wave propagation, free space propagation model, Relating power to electric field, The Three basic propagation mechanism, reflection, ground reflection (Two-Ray) Model, Diffraction, Scattering, Practical link budget design using path loss models, outdoor propagation models, indoor propagation models , signals penetration into building s, ray tracing and site specific modeling. **08 Hours**

Module – IV

Modulation Techniques for mobile radio: Digital modulation- an overview, line coding pulse shaping techniques, geometric representation of modulation techniques, liner modulation technique, constant envelope modulation, combined linear and constant envelope modulation techniques. **08 Hours**

Module – V

Multiple-Accesses (MA) schemes: Introduction, FDMA, TDMA, SDMA, packet radio, capacity of cellular system. **08 Hours**

Course Outcomes:

On completion of this course, students will be able to :

- Describe basics of wireless communication techniques.
- Analyze and describe the fundamentals of cellular concepts.
- Describe mobile radio communication in depth.
- Distinguish between different types of modulation techniques for mobile radio communication.
- Differentiate the types of Multiple Access schemes.

Text Books:

1. Theodore S. Rappaport: “Wireless communications - Principles and Practices”, 2nd Edition, Prentice Hall, 2001, ISBN-10: 0130422320, ISBN-13: 978-0130422323.

Reference Books:

1. Dr. Kamilo Feher: “Wireless digital communication”, Prentice Hall, Har/Dskt, 1995, ISBN-10: 0130986178, ISBN-13: 978-0130986177.
2. William C. Y. Lee: “Mobile Communication Engineering -Theory and applications”, Tata McGraw Hill, 1995, ISBN-10: 0070370397, ISBN-13: 978-0070370395.

E-Resources:

1. <http://nptel.ac.in/courses/117102062/8>
2. <http://nptel.ac.in/courses/117102062/21>
3. http://www.radio-electronics.com/info/cellular/telecomms/cellular_concepts/multiple_access_schemes.php
4. <https://www.elprocus.com/types-of-wireless-communication-applications/>



Artificial Intelligence

Course Code	L:T:P:S	Credits	Exam marks	Exam Duration	Course Type
17ECT742	3:0:0:0	3	CIE:50 SEE:50	3 Hours	EE

Course Objectives:

This course will enable students to

- Apply a given AI technique to a given concrete problem
- Implement non-trivial AI techniques to handle complex problem
- Understand uncertainty and Problem-solving techniques.
- Understand various symbolic knowledge representation to specify domains and reasoning tasks of a situated software agent.
- Understand different logical systems for inference over formal domain

Syllabus

Module – I

Introduction: What is AI? Intelligent Agents: Agents and environment; Rationality; the nature of environment; the structure of agents. Problem solving: Problem-solving agents; Example problems; Searching for solution; Uninformed search strategies.

09 Hours

Module – II

Informed Search, Exploration, Constraint Satisfaction, Adversial Search: Informed search strategies; Heuristic functions; On-line search agents and unknown environment. Constraint satisfaction problems; Backtracking search for CSPs. Adversial search: Games; Optimal decisions in games; Alpha-Beta pruning.

08 Hours

Module – III

Logical Agents: Knowledge-based agents; The wumpus world as an example world; Logic; propositional logic Reasoning patterns in propositional logic; Effective propositional inference; Agents based on propositional logic.

07 Hours

Module – IV

First-Order Logic, Inference in First-Order Logic-1: Representation revisited; Syntax and semantics of first-order logic; Using first-order logic; Knowledge engineering in first-order logic. Propositional versus first-order inference; Unification and lifting

08 Hours

Module – V

Inference in First-Order Logic-2: Forward chaining; Backward chaining; Resolution.

08 Hours

Course Outcomes:

After completion of this course, the students will be able to:

- Design intelligent agents for solving simple gaming by using artificial intelligence.
- Apply non-trivial AI techniques to handle complex problems.
- Apply various symbolic knowledge representation to specific problems.
- Design Knowledge-based agents.
- Understand syntax and semantics of first-order logic.
- Understand inference in first order logic.

Text Book:

1. Stuart Russel, Peter Norvig: Artificial Intelligence A Modern Approach, 2nd Edition, Pearson Education, 2003. (Chapters 1.1, 2, 3.1 - 3.4, 4.1, 4.2, 4.5, 5.1, 5.2, 6.1-6.3, 7, 8, 9, 10, 11.1, 11.2, 11.4, 11.5, 13.1, 13.4, 13.5, 13.6,) ISBN 0-13-103805-2.

Reference Books:

1. Elaine Rich, Kevin Knight: “Artificial Intelligence”, 3rd Edition, Tata McGraw Hill, 2009, ISBN-10: 007087709.
2. Nils J. Nilsson: “Principles of Artificial Intelligence”, Elsevier, 1980, ISBN: 978-3-540-11340-9.

E-Resources:

1. <http://stpk.cs.rtu.lv/sites/all/files/stpk/materiali/MI/Artificial%20Intelligence%20A%20Modern%20Approach.pdf>.
2. <http://www.getfreebooks.com/16-sites-with-free-artificial-intelligence-ebooks>



Micro-Electro-Mechanical Systems (MEMS)

Course Code	L:T:P:S	Credits	Exam marks	Exam Duration	Course Type
17ECT743	3:0:0:0	3	CIE:50 SEE:50	3 Hours	EE

Course Objectives:

This course will enable students to :

- Understand the standard micro fabrication techniques and the issues surrounding them.
- Understand the major classes, components, and applications of MEMS devices/systems.
- Understand the unique requirements, environments, and applications of MEMS.

Syllabus

Module – I

Overview of MEMS and Microsystems: MEMS & Microsystems, Typical MEMS and Micro system products — features of MEMS, The multidisciplinary nature of Microsystems design and manufacture, Applications of Microsystems in automotive industry, health care industry, aerospace industry, industrial products, consumer products and telecommunications. **08 Hours**

Module – II

Scaling Laws in Miniaturization: Introduction to scaling, scaling in geometry, scaling in rigid body dynamics, scaling electrostatic forces, electromagnetic forces, electricity, scaling in fluid mechanics & heat transfer.

Transduction Principles in MEMS and Microsystems: Introduction, Micro sensors — thermal, radiation, mechanical, magnetic and bio-sensors, Micro actuation, MEMS with micro actuators. **08 Hours**

Module – III

Microsystems Fabrication Process: Introduction, Photolithography, Ion-implantation, diffusion, oxidation, CVD, PVD, etching and materials used for MEMS, Some MEMS fabrication processes: surface micro-machining, bulk micromachining, LIGA process, LASER micro machining, MUMPS, FAB-less fabrication **08 Hours**

Module – IV

Micro System Design and Modeling: Introduction, Design considerations: Process design, Mechanical design, Modeling using CAD tools: ANSYS / Multiphysics or Intellisuite or MEMS CAD, Features and Design considerations of RF MEMS, Design considerations of Optical MEMS (MOEMS), Design and Modeling: Case studies: i) Cantilever beam ii) Micro switches iii) MEMS based SMART antenna in

mobile applications for maximum reception of signal in changing communication conditions and iv) MEMS based micro mirror array for control and switching in optical communications. **08 Hours**

Module – V

Micro system packaging: Over view of mechanical packaging of micro electronics micro system packaging, Interfaces in micro system packaging, Packaging technologies. **08 Hours**

Course Outcomes:

On completion of this course, students will be able to :

- Describe new applications and directions of modern engineering.
- Describe the techniques for building microdevices in silicon, polymer, metal and other materials.
- Describe the physical, chemical, biological, and engineering principles involved in the design and operation of current and future microdevices.
- Analyze microsystems technology for technical feasibility as well as practicality.
- Describe the limitations and current challenges in microsystems technology.

Text Books:

1. Tai Ran Hsu: “MEMS and Micro Systems: Design and Manufacture”, Tata McGraw Hill, 2002.
2. Boca Raton: “MEMS and NEMS: Systems, Devices and Structures”, CRC Press, 2002.

Reference Books:

1. J. W. Gardner, V. K. Vardan: “Micro Sensors MEMS and SMART Devices”, John Wiley, 2002.
2. N. Maluf: “Introduction to Micro Mechanical Systems Engineering, Artech House”, Norwood, MA, 2000.

E-Resources:

1. <http://nptel.ac.in/courses/117105082/>
2. <http://www.ece.ubc.ca/course/elec-465>
3. <https://www.mems-exchange.org/MEMS/fabrication.html>
4. <https://www.elprocus.com/understanding-fabrication-mems/>



Tax Management

Course Code	L:T:P:S	Credits	Exam marks	Exam Duration	Course Type
17HOE751	2:0:0:4	3	CIE:50 SEE:50	3 Hours	OE

Course Objectives:

This course will enable students to :

- Familiarise the students with the significance of taxation system.
- Understand the structure of Indian Taxation system.
- Gain knowledge about the practical aspects of Indian taxation.
- Understand the system of computation of tax from Salaries.
- Sketch the recent trends in Indian taxation system.

Syllabus

Module – I

Introduction to taxation system, Objectives of taxation, Factors to be considered for tax planning Canons of taxation, Types of taxation, Direct tax, Indirect tax (Broad perspective only). **07 Hours**

Module – II

Taxation system in India, Types of taxes levied in India, Various heads of income tax (Broad outline only) Basic concepts in taxation, Assessment year, Financial year, assessee, Residential status, Tax liability **08 Hours**

Module – III

Income tax authorities in India, Constitution, Powers, Functions specimen of Form 16, Filing of returns, tax evasion, Penalties for contravening the provisions of income tax. **08 Hours**

Module – IV

System of computation of tax from salaries, Taxable income, Permissible deductions from 80C to 80U Fringe benefits exempted from tax, exempted income under section 10 of Income tax act. **10 Hours**

Module – V

Trends in Indian taxation system, Self assessment, PAN card, Budgetary provisions of the financial year 2017-18 on taxation, GST, Advantages, Problems in implementing GST, Measures to overcome the limitations of GST. **06 Hours**

Course Outcomes:

On completion of this course, students will be able to :

- Gain knowledge about the system of taxation prevailing in the country.
- Compute tax under different heads.
- Gain practical knowledge on filing returns.
- Calculate the payable tax for salaried individuals.
- Gain insight into recent practices on taxation.

Reference Books:

1. Dr. Vinod K. Singhania: “Direct taxes-Law and Practice”, Taxmann Publication.
2. Dr. Mehrotra, Dr. Goyal: “Direct taxes- Law and Practice”, Sahitya Bhavan Publication.
3. “7 lectures-Income tax-I “, VBH.
4. Swaminathan: “Income Tax”, KPH.
5. T.N.Manoharan: “Income tax including VAT”.
6. R.G.Saha, Ushadevi: “Taxation”, HPH.



Assessment of Building Energy Performance

Course Code	L:T:P:S	Credits	Exam marks	Exam Duration	Course Type
17HOE752	2:0:0:4	3	CIE:50 SEE:50	3 Hours	OE

Course Objectives:

This course will enable students to :

- Provide a foundation for performing a building energy audit
- Perform assessment tasks on building energy performance
- Submit a Building EQ rating
- Understand the methods and processes to be performed in the field
- Know the process of certifying professionals in energy assessment

Syllabus

Module – I

Introduction, global energy consumption characteristics and the role of commercial and residential buildings, building energy end use consumption characteristics, impact of time variations in building energy consumption, Building mechanical, electrical, and lighting systems. **08 Hours**

Module – II

Anatomy of typical HVAC systems in commercial buildings , typical primary and secondary HVAC equipment and their role in meeting system requirements , basics of electrical distribution systems and their equipment in commercial buildings, basics of lighting system in buildings, including performance terminology, lighting technologies, energy performance, and the role of day lighting. **08 Hours**

Module – III

Introduction to building energy benchmarking and assessment , differences between benchmarking, labeling programs, and energy and environmental auditing, role of building type and climate zone on energy use, key aspects of ENERGY STAR® Portfolio Manager and other tools for benchmarking, ASHRAE Building EQ As Designed and In Operation ratings, differences between Building EQ and Portfolio Manager, Preliminary Energy Use Analysis (PEA). **08 Hours**

Module – IV

Measuring and monitoring building performance, instrumentation for measuring indoor environmental quality and building energy flows, Perform measurements of indoor environmental quality and building energy flows, accuracy of building measurements, Identify the components of an ASHRAE Level 1 walk through survey and the differences between Level 1, 2, and 3 surveys, Indoor Environment Quality. **08 Hours**

Module – V

Energy Efficiency Measures – Building Envelope and Lighting, role of building envelope characteristics on energy use, energy conservation and energy efficiency measures related to envelope and lighting characteristics, Energy Efficiency Measures – HVAC Systems, energy conservation and energy efficiency measures related to HVAC systems, financial analysis of expected improvements to HVAC systems. **08 Hours**

Course Outcomes:

On completion of the course, the student will be able to:

- Produce an ASHRAE Building EQ In Operation rating for the buildings provided in the class
- Produce a listing of potential Energy Efficiency Measures (EEM) including financial payback analysis
- Perform measurements of indoor environmental quality and HVAC system performance
- Identify different building types and determine the impact of climate on energy use.
- Analyze raw energy consumption data from measured-meter readings

Text Books:

1. ASHRAE Building Energy Quotient Program website.
2. ASHRAE BEAP certification study guide.
3. ASHRAE Standard 105-2014 Standard Methods of Determining, Expressing, and Comparing Building Energy Performance and Greenhouse Gas Emission.

Reference Books:

1. ASHRAE Performance Measurement Protocols for Commercial Buildings: Best Practices Guide.
2. ENERGY STAR® Portfolio Manager website.



Natural Disaster Mitigation and Management

Course Code	L:T:P:S	Credits	Exam marks	Exam Duration	Course Type
17HOE753	2:0:0:4	3	CIE:50 SEE:50	3 Hours	OE

Course Objectives:

This course will enable students to :

- Understand the types of natural and environmental disasters.
- Develop skills in various stages of disaster preparedness, mitigation and management.
- Understand the methodologies for disaster risk assessment.

Syllabus

Module – I

Natural Disasters – Overview: Introduction- Natural Disasters around the world- Natural Disaster Risk Assessment- Earth and its characteristics Human Dimensions of Global environment Change – Disaster mitigation, preparedness, response and recovery comprehensive emergency management Early warning systems and Disaster Preparedness– Rehabilitation, Vulnerable Populations - Logistics and Services, Food, Nutrition and Shelter -Role of UN Red cross and NGOs. **08 Hours**

Module – II

Natural Hazards: Introduction and Review - Natural Disasters -Principles, Elements, and Systems - Geological-Geomorphological aspects, - Earthquake-Geology, Seismology, Characteristics and dimensions– Landslides- Human impact on the mountainous terrain and its relationship with Rainfall, liquefaction etc- Tsunami - Nature and characteristics. **08 Hours**

Module – III

Climate system aspects and Processes: Oceanic, Atmospheric and Hydrologic cycles - Severe Weather & Tornadoes , Cyclones, Floods and Droughts - Global Patterns - Mitigation & Preparation – Drought – Famine- nature and dimensions – Drought Assessment and Monitoring. **08 Hours**

Module – IV

Natural Disaster Communication: Mapping - Modeling, risk analysis and loss estimation – Natural disaster risk analysis - prevention and mitigation - Applications of Space Technology (Satellite Communications, GPS, GIS and Remote Sensing and Information / Communication Technologies (ICT) in Early warning Systems - Disaster Monitoring and Support Centre– Information Dissemination, mobile communication – etc. **08 Hours**

Module – V

Administrative mechanisms: Community and Social organizations – Education and Training – Establishment of capacity building among various stake holders – Government - Educational institutions – Use of Multi-media knowledge products for self education. **08 Hours**

Course Outcomes:

On completion of the course, the student will be able to :

- Learn about the types of natural and environmental disasters and its causes.
- Learn about organizational and Administrative strategies for managing disasters.
- Learn about the early warning systems, monitoring of disasters effect and necessity of rehabilitation.
- Learn about the engineering and non-engineering controls of mitigating various natural disasters.
- Understand the key roles of capacity building to face disaster among government bodies, institutions, NGO's, etc.
- Learn methodologies for disaster risk assessment with the help of latest tools like GPS, GIS, Remote sensing, information technologies, etc.

Text Books:

1. Kovach, Robert L :“Earth’s Fury: An Introduction to Natural Hazards and Disasters”, Englewood Cliffs, N.J., Prentice Hall, 1995.
2. Siddhartha Gautam, K Leelakrishna Rao: “Natural disaster Management”, 3rd Edition, 2012, ISBN: 9381604320.

Reference Books:

1. Arul Jothi, D L Balaji: “Safety And Disaster Management Education in Schools”, 1st Edition, Anmol Publications, 2009, ISBN: 9380252609.

E-Resources:

1. <https://www.publicsafety.gc.ca/cnt/mrgnc-mngmnt/dsstr/bt-dsstr-mtgtn-en.aspx>
2. www.nrdms.gov.in/natural_disaster.asp
3. <https://www.ncbi.nlm.nih.gov> › NCBI › Literature › Bookshelf



Small and Medium Enterprise Management

Course Code	L:T:P:S	Credits	Exam marks	Exam Duration	Course Type
17HOE761	2:0:0:4	3	CIE:50 SEE:50	3 Hours	OE

Course Objectives:

This course will enable students to :

- Understand the various concepts of Entrepreneurship and familiarize them with the understanding of contemporary environment of MSMEs.
- Understand the business Environment to MSMEs.
- Understand the process of Enterprise Creation.
- Understand the effective Business Plan and Institutional Support Mechanism.
- Understand the concepts of marketing management in the MSMEs.

Syllabus

Module – I

Basic Aspects: Concept, nature of Entrepreneur and Entrepreneurship, Distinction between Entrepreneur and Manager, Entrepreneurship, Medium, Small and Tiny Business : Definition, Role in the economy and significance, Changing scenario of MSMEs in the era of Liberalization and Globalization, Competitiveness. **08 Hours**

Module-II

Environment assessment: Political, Legal, Economic, Social, Technological, Global environment, Assessment of business opportunities, Government initiatives and private sector opportunity. **08 Hours**

Module-III

Enterprise Creation: Starting a small industry, Entrepreneurial function or process of starting a new venture based on personal competencies, requirements to start a business venture, Feasibility of the project, Business incubators . **08 Hours**

Module-IV

Business Plan: Developing effective business plan-meaning, benefits of business plan, Timing of the business plan, Length of the business plan, composition of the business plan or detailed project report. Institutional Support Mechanism: District Industries Centre, State Directorate of Industries, SIDBI, NSIC, SISI, KSFC, KIADB, TECSOK.

08 Hours

Module-V

Small Business Marketing: Concept of Marketing, Scope of Marketing, Marketing Mix, Product Mix, Channels of Distribution, Market Segmentation, Role of Middlemen, Distribution Strategies, Sales Promotion, Advertising and Publicity, Packaging Strategies, Branding Strategies. **08 Hours**

Course Outcomes:

On completion of this course, students will be able to :

- Visualize the various concepts of Entrepreneurship and understand of current environment of MSMEs.
- Know the Business Environment with respect to MSMEs.
- Know the Process of Enterprise Creation.
- Prepare Business Plan and Understand the Institutional Support Mechanism.
- Know the marketing management with reference to MSMEs.

Text Books:

1. Shukla. M.B: “Entrepreneurship and Small Business Management”, Kitab Mahal, Allahabad, 2011.
2. Sahay A., V. Sharma: “Entrepreneurship and New Venture Creation”, Excel Books, New Delhi, 2008.
3. Lall, Sahai: “Entrepreneurship”, Excel Books, New Delhi, 2006.
4. S. Anil Kumar: “Small Business and Entrepreneurship”, I.K.International Publishing House Pvt. Ltd., 2008.
5. Kotler, Keller, Koshy, Jha: “Marketing Management”, 13th Edition, Pearson Education.

Reference Book:

1. Wickham, Phillip A: “Strategic Entrepreneurship”, Pitman, UK, 1998.



Occupational Safety and Health Administration

Course Code	L:T:P:S	Credits	Exam marks	Exam Duration	Course Type
17HOE762	2:0:0:4	3	CIE:50 SEE:50	3hours	OE

Course Objectives:

This course will enable students to :

- Understand the occupational health and safety and sector specific occupational health and safety issues.
- Understand the socio-economic aspects of occupational health and safety.
- Understand the health screening measures.
- Understand the legal Provisions on Occupational Health and Safety.
- Understand the participatory Research and Occupational Health.

Syllabus

Module – I

Introduction to Occupational Health and Safety: Definition and Context of OHS, Objectives and Principles of OHS, Workplace and Health Occupational Health, Hygiene and Ergonomics.

Sector Specific Occupational Health and Safety Issues: Health and Safety Risks in Mining, Health Hazards in Electronic Industry, Health Hazards in Food Processing Industry, Health Hazards in Other Industries. **07 Hours**

Module – II

Socio-Economic aspects of Occupational Health and Safety: Women's occupational and health safety, Child labour issues in occupational health and safety, Health issues in the unorganized sector.

Basics of Preventive Techniques: Definition of Accident, Accident Analysis, Monitoring of Hazards, Reporting and Investigation of Accidents. **08 Hours**

Module – III

Health Screening Measures: Stages of Medical Examination, Occupational History, Pulmonary Function Test (PFT), Noise Induced Hearing Loss (NIHL). **07 Hours**

Module – IV

Legal Provisions on Occupational Health and Safety: Overview of existing OHS Legislations in India, The Factories Act, The Mines Act, The Workmen's Compensation Act, The Employee's State Insurance Act. **07 Hours**

Module-V

Participatory Research and Occupational Health: Philosophy of Participatory Research (PR) Analysis based on PR Methodologies Conducting Participatory Research for OHS. **07 Hours**

Course Outcomes:

On completion of this course, students will be able to :

- Develop the ability to know the occupational health and safety.
- Have the knowledge of the socio-economic aspects of occupational health and safety.
- Demonstrate purpose of health screening measures.
- Know the legal Provisions on Occupational Health and Safety.
- Participate in Research and Occupational Health.

References:

1. International Labour Organization. Mining: a hazardous work [Internet]. ; 2015 ([cited 2015 Feb 2]. Available from: http://www.ilo.org/safework/areasofwork/hazardous-work/WCMS_124598/lang--en/index.htm
2. Gyekye, S.A. Workers' perceptions of workplace safety: an African perspective. *Int J Occup Saf Ergon*. 2006;12:31–42. Crossref | PubMed | Scopus (4)
3. Amponsah-Tawiah, K., Jain, A., Leka, S., Hollis, D., Cox, T. Examining psychosocial and physical hazards in the Ghanaian mining industry and their implications for employees' safety experience. *J Safety Res*. 2013;45:75–84. Crossref PubMed | Scopus (5)
4. Owiredu D. Annual chamber of mines presidential review. 83rd Annual General Meeting of the Ghana Chamber of Mines [Internet]. 2011 [cited 2014 Mar 1]. Available from: <http://www.ghanachamberofmines.org>.
5. Helliwell, J.F., Putnam, R.D. The social context of wellbeing. *Philos Trans R Soc Lond B Biol Sci*. 2004;35:1435–1446. Crossref | Scopus (550)
6. Bhagawati, B. Basics of occupational safety and health. *IOSR J Environ Sci Toxicol Food Technol*. 2015;9:91–94.
7. Amponsah-Tawiah, K., Dartey-Baah, K. Occupational health and safety: key issues and concerns in Ghana. *Int J Bus Soc Sci*. 2011;14:120–126.
National Safety Council. Injury facts. NSC, Itasca (IL); 2004.

Animation and Multimedia Engineering

Course Code	L:T:P:S	Credits	Exam marks	Exam Duration	Course Type
17HOE763	2:0:0:4	3	CIE:50 SEE:50	3 Hours	OE

Course Objectives:

This course will enable students to:

- Understand the basics of Animation.
- Understand computer animation using characters.
- Learn how to create quality animation characters.
- Learn about volume construction and action made from face, gestures.
- Understand Acting and Sketching techniques.

Syllabus

Module – I

Introduction to Animation: History of Animation, The Origins of Animation, Types of Animation, Terms used in Animation, Basic Principles of Animation.

Introduction to equipment required for Animation: Animator’s Drawing Tools, Rapid Sketching and Drawing, Developing Animation Character. **07 Hours**

Module – II

Developing the characters with computer animation: Anatomy and Body Language, 2-D virtual drawing for animation.

Motion studies: : Thumbnails, sequential movement drawing, drawing for motion. **08 Hours**

Module – III

Essentials and qualities of good animation characters: Three dimensional drawings of characters.

Skills and Basic proportions: Visual and creative development of an artist, how to draw gestures, Heads, Rotation in Arcs, Key Lines, Perspective. **08 Hours**

Module – IV

Volume Construction: Balance, Muscles, Light and shade.

Shape and Action: Hands and Legs, Foreshortening, Facial expressions. **08 Hours**

Module – V

Acting and Sketching techniques: Introduction to Acting, Modeling, Sketching from Acting, Sketching from live models, Introduction to Rapid Sketching Techniques, Sketching from Memory, live action. **09 Hours**

Course Outcomes:

On completion of this course, students will be able to :

- Recognize the basics of animation along the tools.
- Develop characters with computer animation.
- Develop 3D drawings of characters and acquire skills regarding basic level of sketching.
- Explain Foreshortening, Facial expressions.
- Develop small animation characters by using acting and sketching techniques.

Text Book:

1. Chris Patmore: “The Complete Animation course: The Principles, Practice and Techniques of Successful Animation”, (Chapters 1-10), Barons Educational Series New York, 2003, ISBN-13: 978-0764123993.

Reference Books:

1. Frank Thomas, Ollie Johnston: “The Illusion of Life by Walt Disney”, Abbeville Press, 1981.
2. Daniel Carter, Michael Courtney: “Anatomy for the Artist: A Comprehensive Guide to Drawing the Human Body, A Complete Guide”, 2011.

E-Resources:

1. [http:// www.animationmentor.com/](http://www.animationmentor.com/)
2. <https://www.blopanimation.com/animation-for-beginners/>
3. <https://robots.thoughtbot.com/css-animation-for-beginners>



Power Electronics Lab

Course Code	L:T:P:S	Credits	Exam marks	Exam Duration	Course Type
17ECL77	1:0:2:0	2	CIE:50 SEE:50	3 Hours	FC

Course Objectives:

This course will enable students to :

- Understand the working knowledge of different triggering circuits, DIAC and TRIAC.
- Understand the operation and characteristics of Stepper motor and Inverter.

List of Experiments:

1. SCR Characteristics
2. TRIAC Characteristics
3. MOSFET Characteristics
4. IGBT Characteristics
5. RC Triggering circuit – HWR & FWR
6. UJT Triggering of SCR
7. Oscillation Chopper circuit
8. UJT Triggering circuit – HWR & FWR
9. Digital Firing circuit
10. AC Voltage control by using TRIAC and DIAC
11. Single Phase FWR
12. Impulse Commuted Chopper
13. Speed control of a Stepper motor
14. Series Inverter
15. Parallel Inverter

Course Outcomes:

On completion of this course, students will be able to :

- Understand the fundamental concepts of MOSFET, IGBT and SCR.
- Understand the basics of UJT HWR and FWR.
- Discuss UJT triggering of SCR.
- Understand Stepper motor concepts.
- Describe various types of inverters.

Data Communication Lab

Course Code	L:T:P:S	Credits	Exam marks	Exam Duration	Course Type
17ECL78	1:0:2:0	2	CIE:50 SEE:50	3 Hours	FC

Course Objectives:

This course will enable students to :

- Understand the practical aspects of techniques used in data communication
- Understand the Serial communication techniques for data communication

List of Experiments:

1. Simulate bit stuffing and destuffing
2. Character stuffing
3. Encryption and decryption
4. Cyclic Redundancy check
5. Simulate short path
6. Minimum spanning tree
7. Serial Communication using RS-232 (Synchronous and Asynchronous)
8. Serial Communication using telephone cable
9. Telnet
10. Remote host access

Course Outcomes:

On completion of this course, students will be able to :

- Understand different data stuffing techniques used in communication.
- Analyze basic encryption techniques.
- Discuss spanning tree.
- Understand serial communication techniques.
- Describe telnet basics and remote access.



Project Phase-I and Seminar

Course Code	L:T:P:S	Credits	Exam marks	Exam Duration	Course Type
17ECP79	0-0-6-0	3	100	3 Hours	FC

Phase	Activity	Credits
I	Batch formation, project identification, literature survey, finalization of problem statement with objectives and outcomes, Synopsis submission, Preliminary seminar for the approval of selected topic and objectives	3

❧ ❧ ❧ ❧ ❧

Eighth Semester B.E. – Syllabus

Phase	Activity	Credits
II	Design, Theoretical/experimental investigation and Mid-term seminar to review the progress of the work and documentation (Mid term report).	4
III	Completion of the project work, participation in the project exhibition, Submission of project report Final Internal seminar and demonstration, Publications.	4
	Evaluation and Viva-voce	5 +5

Program Educational Objectives (PEOs)

Electronics & Communication Engineering graduates are expected to fulfill the following PEOs after few years of their graduation.

PEO1	Graduates of Electronics and Communication engineering will be using the basic academic knowledge of design and analysis required in the industry for sustainable societal growth.
PEO2	Graduates of Electronics and Communication engineering will demonstrate the technical competence based on modern tools.
PEO3	Graduates in Electronics and Communication engineering will demonstrate good communication skills, dynamic leadership qualities with concern for environmental protection.
PEO4	Electronics and Communication engineering graduates will be capable of pursuing higher studies, take up research and development work blended with ethics and human values.
PEO5	Electronics and Communication engineering graduates will have the ability to become employable and entrepreneurs thereby switching over from responsive engineering to creative engineering.

Program Outcomes (POs)

PO1	Engineering Knowledge: Apply the knowledge of mathematics, science, engineering fundamentals, and electronics and communication engineering principles to the solution of complex problems in electronics and communication engineering.
PO2	Problem Analysis: Identify, formulate, research literature, and analyze complex electronics and communication engineering problems reaching substantiated conclusions using first principles of mathematics, and engineering sciences.
PO3	Design/ Development of solutions: Design solutions for complex electronics and communication engineering problems and design system components or processes that meet the specified needs with appropriate consideration for the public health and safety, and the cultural, societal, and environmental considerations.

PO4	Conduct Investigations of Complex problems: Use research-based knowledge and research methods including design of experiments, analysis and interpretation of data, and synthesis of the information to provide valid conclusions related to electronics and communication engineering problems.
PO5	Modern Tool Usage: Create, select, and apply appropriate techniques, resources, and modern engineering and IT tools including prediction and modelling to complex electronics and communication engineering activities with an understanding of the limitations.
PO6	The Engineer and Society: Apply reasoning informed by the contextual knowledge to assess societal, health, safety, legal and cultural issues and the consequent responsibilities relevant to the professional electronics and communication engineering practice.
PO7	Environment and Sustainability: Understand the impact of the professional electronics and communication engineering solutions in societal and environmental contexts, and demonstrate the knowledge of, and need for sustainable development.
PO8	Ethics: Apply ethical principles and commit to professional ethics and responsibilities and norms of the electronics and communication engineering practice.
PO9	Individual and Team Work: Function effectively as an individual, and as a member or leader in diverse teams, and in multidisciplinary settings.
PO10	Communication: Communicate effectively on complex electronics and communication engineering activities with the engineering community and with society at large, such as, being able to comprehend and write effective reports and design documentation, make effective presentations, and give and receive clear instructions.
PO11	Project Management and Finance: Demonstrate knowledge and understanding of the engineering and management principles and apply these to one's own work, as a member and leader in a team, to manage projects and in multidisciplinary environments.
PO12	Life-Long Learning: Recognize the need for, and have the preparation and ability to engage in independent and life-long learning in the broadest context of technological change.



NAGARJUNA

COLLEGE OF ENGINEERING & TECHNOLOGY

An Autonomous College under VTU

**DEPARTMENT OF ELECTRONICS &
COMMUNICATION ENGINEERING**

VISION

To transform the students as leaders in Electronics & Communication Engineering to achieve professional excellence in the challenging future

MISSION

- M1: To create an environment for the students to have strong academic fundamentals and enable them to be life-long learners.
- M2: To provide modern tools to the students in the field of electronics and communication to meet the real-world challenges.
- M3: To develop Communication skill, leadership qualities, team work and skills for continuing education among the students.
- M4: To inculcate Ethics, Human values and skills for solving societal problems and environmental protection.
- M5: Validate engineering knowledge through innovative research projects to enhance their employability and entrepreneurship skills.

III & IV Semesters

Scheme and Syllabus

With effect from Academic Year 2019-20

Third Semester B.E. - Scheme

Sl. No.	Course Code	Course	Teaching Dept.	L:T:P:S (Hrs/week)	Total Credits	Marks
1	18ECM31	Integral Transforms with SCILAB (IC)	Maths	3:0:2:0	4	100
2	18ECT32	Analog Electronic Circuits	EC	4:0:0:0	4	100
3	18ECI33	Digital Electronic Circuits(IC)	EC	3:0:2:0	4	100
4	18ECI34	Network Analysis (IC)	EC	3:0:2:0	4	100
5	18ECT35X	Foundation Elective – I	EC	4:0:0:0	4	100
6	18ECL36	Analog Electronic Circuits Laboratory	EC	1:0:2:0	2	100
7	18CSD37	Career Skill Development Programme	S&H	1:2:0:0	2	100
8	18CPH38	Constitution of India and Professional Ethics and Human Rights	S&H	1:0:0:0	1	100
TOTAL				20-2-8-0	25	800

Foundation Elective – I (IC)

Sl. No.	Course Code	Course
1	18ECT351	Engineering Electromagnetics
2	18ECT352	Data Structures with C
3	18ECT353	Electronic Devices

IC – Integrated Course

L – Lecture

T-Tutorials

P-Practical

S – Self Study

Fourth Semester B.E. – Scheme

Sl. No.	Course Code	Course	Teaching Dept	L:T:P:S (Hrs/week)	Total Credits	Marks
1	18ECM41	Fourier Series and Probability Theory (IC)	Maths	3:0:2:0	4	100
2	18ECT42	Fundamentals of HDL	EC	4:0:0:0	4	100
3	18ECI43	Signals and Systems (IC)	EC	3:0:2:0	4	100
4	18ECI44X	Foundation Elective – II (IC)	EC	3:0:2:0	4	100
5	18EET45X	Engineering Elective – III	EC	4:0:0:0	4	100
6	18ECL46	Fundamentals of HDL Lab	EC	1:0:2:0	2	100
7	18CSD47	Technical Report Writing & IRDP	S&H	1:2:0:0	2	100
8	18KAK38 / 18KAK38	Vyavaharika Kannada / Adalitha Kannada	S&H	1:0:0:0	1	100
TOTAL				20-2-8-0	25	800

Foundation Elective - II (IC)

Sl. No.	Course Code	Course
1	18ECI441	Linear Integrated Circuits & Applications
2	18ECI442	Transmission Lines and Wave Guides (TLWG)
3	18ECI443	Electronic Instrumentation

Engineering Elective - III

Sl. No.	Course Code	Course
1	18EET451	Renewable Energy Resources
2	18EET452	Introduction to Cyber Security and Cyber Laws
3	18EET453	Management Information System
4	18EET454	Environmental Air Pollution

IC – Integrated Course

L – Lecture

T-Tutorials

P-Practical

S – Self Study

Integral Transforms And Fourier Series (IC)

Course Code	L:T:P:S	Credits	Exam marks	Exam Duration	Course Type
18ECM31	3:0:2:0	4	CIE:50 SEE:50	3 Hours	BS

Course Objectives:

This course will enable students to :

- Develop the knowledge of solving the engineering problems by using software.
- Obtain the knowledge of Integral Transforms in solving the engineering problem.
- Have the efficiency in expressing a periodic function in terms of infinite trigonometric series.

Syllabus

Module – I

Sci Lab: Introduction to SciLab, General environment of SciLab and Console, Work space and Working directory, Basic calculations in SciLab, Matrix operations, Functions in SciLab, SciLab programming, plotting of graphs. **07 Hours**

Module – II

Laplace Transform: Laplace transform of elementary functions (Formulae only). Properties (without proof), transform of derivatives, multiplication by t^n and division by t (Statements only) - Problems. Laplace transforms of periodic functions, unit step functions and unit impulse function (without proof) –Problems. **08 Hours**

Module – III

Inverse Laplace Transforms: Inverse Laplace transforms of standard functions. (Formulae only). Inverse Laplace transform by using completing the squares, partial fractions, shifting property and differentiation - problems. Convolution theorem (statement only) - problems. Applications - solution of linear differential equations with initial conditions- problems. **08 Hours**

Module – IV

Fourier Transforms: Complex Fourier transforms, Properties (without proof), Fourier Sine and Cosine transforms and their inverse transforms. Finite Fourier Transforms - problems. **08 Hours**

Module – V

Fourier Series : Periodic functions, Dirichlet's conditions, Euler's Formulae (without proof), Fourier series of periodic functions of period $2l$ and 2π , Half range Fourier series - problems. **08 Hours**

List of Sci Lab Experiments

Expt. No.	Experiments
1	Introduction of Sci Lab and Basic operations in Sci Lab.
2	Simple numerical calculations.
3	Construction of vectors and matrices.
4	Matrix operations
5	Roots of polynomials and Solutions of system of equations
6	Plotting of graphs
7	Numerical methods
8	Numerical methods

Course Outcomes:

On completion of this course, the students are able to:

- Use the SCILAB in solving the various types of engineering problems.
- Find the Laplace transform of different types of functions.
- Use the Laplace transform and inverse Laplace Transform in solving various types engineering application problems.
- Use the Fourier Transform in solving various types engineering application problems.
- Find the Fourier series of periodic functions.

Text Books:

1. Dr. B.S. Grewal: "Higher Engineering Mathematics", (Chapters 10,21, 22), Khanna Publishers, New Delhi, 42nd Edition, 2012, ISBN:9788174 091955.
2. N.P. Bali and Dr. Manish Goyal: "A Text Book of Engineering Mathematics", (Chapters 10, 18, 20), Laxmi Publications (P) Ltd., New Delhi, 9th Edition, 2014, ISBN: 9788131808320.
3. SCILAB Group: "Introduction to SCILAB, A Users Guide" .

Reference Books:

1. Erwin Kreyszig: “Advanced Engineering Mathematics”, Wiley Pvt. Ltd. India, New Delhi, 9th Edition, 2011, ISBN 13: 9788126531356.
2. B.V. Ramana: “Higher Engineering Mathematics”, Tata Mc Graw – Hill Publishing company Limited, New Delhi, 2nd Reprint, 2007, ISBN 13: 978-0-07063417-0.
3. Stormy Attaway: “A practical introduction to programming and problem solving”, Elsevier, Boston, 2nd Edition.

E-Resources:

1. <http://bookboon.com/en/essential-engineering-mathematics-ebook>
2. <https://www.free-ebooks.net/ebook/essential-engineering-mathematics>
3. <https://www.scilab.org/resources/documentation/books>
4. <https://archive.org/details/AdvancedEngineeringMathematics10thEdition>
5. https://mars.uta.edu/mae3183/simulation/introscilab_baudin.pdf



Analog Electronic Circuits

Course Code	L:T:P:S	Credits	Exam marks	Exam Duration	Course Type
18ECT32	4:0:0:0	4	CIE:50 SEE:50	3 Hours	FC

Course Objectives:

This course will enable students to:

- Understand the working principle of diodes and its applications.
- Illustrate the significance of transistor equivalent circuit models, equations for various configurations.
- Perform AC analysis of amplifiers.
- Examine the operation of different power amplifiers.
- Evaluate and design feedback amplifiers and oscillators.

Syllabus

Module – I

Diode Circuits : Diode Resistance, Diode equivalent circuits, Transition and diffusion capacitance, Reverse recovery time, Load line analysis, Clippers and clampers.

10 Hours

Module – II

Transistor Biasing : Operating point, Fixed bias circuits, Emitter stabilized bias circuits, and Voltage divider biased circuits, DC bias with voltage feedback, Miscellaneous bias configurations, Bias stabilization- General expression, Fixed Bias, Emitter bias and Voltage divider type bias.

10 Hours

Module – III

Transistor at Low Frequencies: BJT transistor modeling, CE Fixed bias configuration, Voltage divider bias, Emitter follower, CB configuration, Collector feedback configuration, Analysis of circuits re model. Darlington Emitter Follower.

Transistor Frequency Response: General Frequency considerations, Low frequency response, Miller effect capacitance.

12 Hours

Module – IV

Power Amplifiers : Definitions and amplifier types, Series fed class A amplifier, Transformer coupled Class A amplifiers, Class B amplifier operations, Class B amplifier, Class C and Class D amplifier circuits.

10 Hours

Module – V

Feedback and Oscillator Circuits: Feedback concept, Feedback connections type. Oscillator operation, Phase shift oscillator, Wien bridge oscillator, Tuned oscillator circuits, Crystal oscillator (BJT version only), Simple design methods of oscillators.

10 Hours

Course Outcomes :

On completion of this course, students should be able to:

- Analyze the diode characteristics and design the Clipping and Clamping circuits.
- Illustrate the various BJT configurations, biasing and stability methods.
- Evaluate the transistor behavior at low frequencies and its effects.
- Explain and design small signal and power amplifier circuits.
- Describe the feedback concepts and design different types of oscillator circuits for the specified frequency.

Text Book:

1. Robert L. Boylestad and Louis Nashelsky: "Electronic Devices and Circuit Theory", (Chapters 1,2,4,5,9,12,14), 10th Edition, PHI/Pearson Education, 2012, ISBN: 978-81-317-6459-6.

Reference Books:

1. Jacob Millman and Christos C. Halkias: "Integrated Electronics", 2nd Edition, Tata - McGraw Hill, 2010, ISBN-978-007-015142-0.
2. David A. Bell: "Electronic Devices and Circuits", 5th Edition, PHI, 2008, ISBN: 978-0-19-569340-9.
3. U. B. Mahadevaswamy: "Analog Electronics Circuits: A Simplified Approach", 1st Edition, Pearson/Saguine, 2010, ISBN: 978-81-317-3234-2.

E-Resources:

1. <http://www.allaboutcircuits.com/textbook/semiconductors/chpt-3/introduction-to-diodes-and-rectifiers/>
2. <http://fourier.eng.hmc.edu/e84/lectures/ch2/node4.html>
3. <http://www.allaboutcircuits.com/video-lectures/transistor-biasing/>



Digital Electronic Circuits (IC)

Course Code	L:T:P:S	Credits	Exam marks	Exam Duration	Course Type
18ECI33	3:0:2:0	4	CIE:50 SEE:50	3 Hours	FC

Course Objectives:

This course will enable students to:

- Understand the concepts of Boolean algebra and Boolean expression minimization techniques.
- Analyze the operations of combinational logic circuits like adder, subtractor, Multiplexers, Decoders, Encoders and Comparator.
- Understand the operation of different types of Flip-Flops.
- Analyze and design the functioning of different types of counters and Shift registers.
- Construct the state diagrams, state table and state equations for Mealy and Moore state models.

Syllabus

Module – I

Simplification of Boolean functions :

Definition of combinational logic, Canonical forms, Generation of switching equations from truth tables, Karnaugh maps-three, four, Incompletely specified functions (Don't Care terms), Simplifying Max term equations, QuineMc-Clusky minimization technique, Map entered variable.

08 Hours

Module – II

Combinational Logic Circuits :

General approach to Combinational Logic Design, Parallel adder and subtractor, carry look ahead adder, Magnitude Comparator, Decoders, Encoders and Multiplexers.

08 Hours

Module – III

Flip-Flops and Simple Flip –Flops Applications :

Basic Bistable Element, Latches, SR Latch, Application of SR Latch, A Switch Debouncer, The SR Latch, The gated SR Latch, The gated D Latch, The Master Slave Flip-Flops (Pulse-Triggered Flip-Flops): The Master-Slave SR Flip-Flops, The Master-Slave JK Flip-Flop, Edge Triggered Flip-Flop: The Positive Edge-Triggered D Flip-Flop, Negative-Edge Triggered D Flip-Flop.

08 Hours

Module – IV**Sequential Circuits :**

Characteristic Equations, Registers, Counters - Binary Ripple Counters, Synchronous Binary counters, Counters based on Shift Registers, Design of Asynchronous counters, Design of a Synchronous Mod-N Counter using clocked JK, D and T Flip-Flops.

08 Hours**Module – V****Sequential Design :**

Introduction, Mealy and Moore Models, State Machine Notation, Synchronous Sequential Circuit Analysis, construction of state diagram.

07 Hours**List of Experiments**

1.	3-bit Binary-to-Gray and Gray-to-Binary Converter using gates.
2.	a) Full Adder and Full Subtractor using gates. b) 4-bit parallel adder/subtractor using IC 7483.
3.	MUX/DE-MUX and Comparators using ICs (74153, 74138, 74139, 7485).
4.	Truth Table verification of FFs using NAND gates: SR and JK-type.
5.	Study of FFs using ICs: JK, T, and D-types (7476).
6.	Mod N counters using ICs (7493, 7490).
7.	Ring Counter and Johnson Counter using IC 7495.

Course Outcomes :

On completion of this course, students should be able to:

- Determine an optimal solution for a given digital problem using K – Maps, QuineMc-Clusky minimization technique and Map entered variable method.
- Design combinational digital circuits using adder, subtractor, Multiplexers, Decoders, Encoders and Comparator for the given specifications.
- Explain the functioning of different types of Latches, Pulse-Triggered and Edge Triggered Flip-Flops.
- Design Registers, Asynchronous and Synchronous counters for the given specifications.
- Develop the appropriate Mealy and Moore Finite State Machine.

Text Books:

1. John M Yarbrough: "Digital Logic Applications and Design", 3rd Edition, Cengage Learning, New Delhi, Reprint, 2012, ISBN-13: 978-81-315-0058-3, ISBN-10: 81315-0058-6.
2. Donald D Givone: "Digital Principles and Design", 1st Edition, Tata McGraw Hill, New Delhi, Reprint, 2005, ISBN: 0-07-052906-X.

Reference Books:

1. Charles H Roth: "Fundamentals of logic design", 5th Edition, Thomson, New Delhi, Reprint, 2007, ISBN: 81-315-0043-8.
2. M. Morris Mano and Charles R. Kime: "Logic and computer design Fundamentals", 2nd Edition, Pearson, Reprint, 2005, ISBN: 81-7808-334-5.

E-Resources:

1. <http://nptel.ac.in/courses/117106086/>
2. <http://www.asic-world.com/digital/tutorial.html>
3. <https://www.wiziq.com/tutorials/digital-electronics>



Network Analysis(IC)

Course Code	L:T:P:S	Credits	Exam marks	Exam Duration	Course Type
18ECI34	3:0:2:0	4	CIE:50 SEE:50	3 Hours	FC

Course Objectives:

This course will enable students to:

- Understand the fundamental concepts and techniques for solving AC and DC networks.
- Study the concepts of Network topology using tie-set and cut-set schedules.
- Evaluate the practical importance of Network Theorems and their applications.
- Understand the Resonant Circuit modules considering Frequency response of series and parallel Circuits.
- Apply the Laplace transform in electrical circuit analysis.

Syllabus

Module – I

Basic Concepts : Practical sources, Source transformations, Network reduction using Star – Delta transformation, Loop and node analysis with linearly dependent and independent sources for DC and AC networks, Concepts of super node and super mesh. **08 Hours**

Module – II

Network Topology : Graph of a network, Concept of tree and co-tree, incidence matrix, tie-set and cut-set schedules, Formulation of equilibrium equations in matrix form. **08 Hours**

Module – III

Network Theorems I : Thevenin's and Norton's theorems and Maximum Power transfer theorems.

Network Theorems II : Superposition, Reciprocity and Millman's theorems. **08 Hours**

Module – IV

Resonant Circuits and Two-Port Networks : Series and parallel resonance, frequency-response of series and parallel circuits, Q-factor, Bandwidth.

Two port network parameters : Definition of z , y , h and transmission parameters. **08 Hours**

Module – V

Laplace Transformation : Definition of Laplace transform, Singularity functions, Functional Transforms, Properties of Laplace Transforms, Inverse Laplace Transform. **08 Hours**

List of Experiments

1. Verification of Thevenin's Theorem.
2. Maximum Power Transfer theorem for DC Circuits.
3. Characteristics of Series and Parallel resonant circuits.
4. Verification of Norton's Theorem.
5. Verification of superposition Theorem for DC circuits.
6. Analyze the Z-transforms for the resistive two port network.

Course Outcomes :

On completion of this course, students should be able to:

- Apply nodal and mesh analysis techniques to various electric circuits.
- Solve electrical networks using tie-set and cut-set schedule.
- Apply various network theorems to simplify electric circuits.
- Design resonant circuits pertaining to its applications with specified low frequencies.
- Evaluate electric circuits using the Laplace transformation and its theorems.

Text Books:

1. M. E. Van Valkenburg: "Network Analysis", (Chapters 1-5), 3rd Edition, Pearson Prentice Hall, New Delhi, 1974. ISBN: 978-81-203-0156-6.
2. W. H. Hyatt Jr., and J. E. Kemmerly, S. M. Durbin: "Engineering Circuit Analysis", 7th Edition, Tata McGraw Hill, New Delhi, 2011, ISBN: 978-0-07-015385-1.

Reference Books:

1. M. Nahvi, J. A. Edminister: "Electric Circuits", 10th Edition, Tata-McGraw Hill, New Delhi, 2007, ISBN 0-07-463591-3.
2. C.K. Alexander, M. N O Sadiku: "Fundamentals of Electric Circuits", 3rd Edition, Tata McGraw Hill, New Delhi, 2007, ISBN: 978-0-07-064803-6.

E-Resources:

1. <http://www.allaboutcircuits.com/textbook/>
2. <http://fourier.eng.hmc.edu/e84/lectures/ch2/node3.html>
3. <http://tutorial.math.lamar.edu/Classes/DE/LaplaceIntro.aspx>
4. <http://fourier.eng.hmc.edu/e84/lectures/ch2/node4.html>



Engineering Electromagnetics

Course Code	L:T:P:S	Credits	Exam marks	Exam Duration	Course Type
18ECT351	4:0:0:0	4	CIE:50 SEE:50	3 Hours	FE

Course Objectives:

This course will enable students to:

- Understand the fundamental concepts and various laws in Electric Fields.
- Study the basic concepts of Energy, Potential and Boundary conditions.
- Analyze the concepts of current and current density in Electric fields.
- Learn the importance of Poisson's and Laplace theorems and their applications.
- Analyze the basic principles of Maxwell's equations and Uniform Plane waves.

Syllabus

Module – I

Electrostatics: Introduction, Coulomb's Law and field intensity, Electric flux density, Gauss's Law, Applications of Gauss's Law, Divergence theorem. **10 Hours**

Module – II

Energy and potential: Electric potential, Gradient of a scalar quantity, Relationship between Electric field and potential, Electric Dipole and flux. Energy and potential in a moving point charge in an Electric Field, the Line Integral, definition of potential difference and potential, the potential field of a point charge and system of charges, Potential gradient, Energy density in an electrostatic field. **10 Hours**

Module – III

Conductors and capacitance: Current and current density, Continuity of current, metallic conductors, Conductor properties and boundary conditions, Capacitance, several capacitance examples, capacitance of a two wire line. **11 Hours**

Module – IV

Poisson's and Laplace's Equations: Derivations of Poisson's and Laplace's Equations, The Steady Magnetic Field: Biot-Savart Law, Ampere's Circuital Law, Curl, Stokes Theorem, Magnetic Flux and Magnetic Flux density. **11 Hours**

Module – V

Time varying fields and Maxwell's equations: Faraday's law, displacement current, Maxwell's equation in point and Integral form.

Uniform plane wave: Wave propagation in free space and dielectrics, The Pointing vector and power considerations. **10 Hours**

Course Outcomes :

On completion of this course, students should be able to:

- Restate and interpret the basics of Vectors, Coordinate systems and Electrostatics.
- Discuss the concepts of Energy, Potential and relationship between electric potential of a moving point charges.
- Discuss the concepts of Current, Current density and parallel capacitance for the various boundary conditions.
- Analyzing basic theory of Poisson's, Laplace's equations and magnetic fields using various laws.
- Apply the Maxwell's equations, relationship between Maxwell's equations and Uniform Plane wave.

Text Book:

1. William H Hayt Jr. and John A Buck: "Engineering Electromagnetic", 8th Edition, Tata McGraw-Hill, 2006, ISBN-13: 978-0071244497, ISBN-10: 0071244492.

Reference Books:

1. John Kraus: "Electromagnetics with Applications", 5th Edition, Tata Mc-Graw Hill, 1999, ISBN-13: 978-0072899696, ISBN-10: 0072899697.
2. Edward C. Jordan and Keith G Balmain: "Electromagnetic Waves and Radiating Systems", 2nd Edition, PHI, New Delhi, 1968, ISBN: 9780132499958.

E-Resources:

1. <http://hyperphysics.phy-astr.gsu.edu/hbase/electric/maxeq.html>
2. <http://www.maxwells-equations.com/>
3. <http://mathworld.wolfram.com/CoordinateSystem.html>
4. http://edndoc.esri.com/arcsde/9.1/general_topics/what_coord_sys.html



Data Structure with C

Course Code	L:T:P:S	Credits	Exam marks	Exam Duration	Course Type
18ECT352	4:0:0:0	4	CIE:50 SEE:50	3 hours	FE

Course Objectives:

This course will enable students to :

- Understand the basics of structures and pointers.
- Apply the concept of data structure for a specified application.
- Understand the impact of data structures on the performance of programs.
- Explore various data structure concepts, such as stacks, queues, linear lists, heaps, trees and graphs
- Apply the concept of various binary search algorithms.

Syllabus

Module – I

Structures and Unions : Introduction, Defining a structure, Declaring structure variables, Accessing structure members, Structure initialization, Size of structures, Copying and comparing structure variables, Array of structures, Arrays within structures, Structures within structures, Unions.

Pointers : Introduction, Accessing the variable and its address through the pointer, Declaring and initialization of pointer variables, Array of pointers, Pointer as function arguments, Functions returning pointers, Dynamic memory allocation - Introduction, malloc(), calloc(), realloc()). **11 Hours**

Module – II

Stack : Definition, Representing stacks in C - Implementing the PUSH and POP operation with example and testing for exceptional conditions, Infix, Postfix, and Prefix expressions. Program to evaluate a postfix expression with example, Program to convert an expression from infix to postfix.

Recursion : Definition, Recursion in C with examples. **Queues**: The Queue and its sequential representation, Implementation of queues in C, Insert and delete operations, Priority queue, Array implementation of a priority queue. **10 Hours**

Module – III

Linked Lists : Introduction, Inserting and removing nodes from a list, Linked list implementation of stacks, queues , List operations : getnode and freenode. Lists in C - Array implementation of lists and its limitations, Circular lists: Primitive operations on circular lists, implement stack and queue as a circular list, Doubly linked lists: Inserting and removing nodes from a list, Addition of long integer using Doubly linked lists. **11Hours**

Module – IV

Trees : Introduction, Binary trees, Binary tree traversals: preorder, inorder and postorder, Threaded binary trees : insertion and deletion of a node, Heaps: insertion and deletion of a node, Max heap and min heap, Binary search trees: insertion and deletion of a node, searching a key element in binary search tree. **10 Hours**

Module – V

Efficient Binary Search Trees : Graph Introduction, Abstract data type of graph, AVL trees - insertion and deletion of node, Red-black trees: insertion and deletion of a node, Splay trees: insertion and deletion of a node. **10 Hours**

Course outcomes:

After studying this course, students will be able to:

- Describe the concepts of structures and pointers.
- Explain and illustrate the various stack and queue operations.
- Interpret and design the programs using linked lists.
- Implement binary trees, threaded binary trees, binary search trees using C language.
- Implement AVL trees, Red-black trees and Splay trees using C language

Text Book:

1. Ellis Horowitz, SartajSahni, Susan Anderson-Freed : “Fundamentals of Data Structures in C”, (Chapters 1,2,3,4,5,6,9,10), Universities Press-India, 2nd Edition, 2008, ISBN-13: 978-8173716058

Reference Books:

1. Thomas H. Cormen, Charles E. Leiserson, Ronal L. Rivest, Clifford Stein: “Introduction to Algorithms”, PHI Learning Private Limited-India, 3rd Edition, 2010, ISBN-13: 978-8120340077
2. Aaron M. Tenenbaum: “Data Structures using C”, Pearson Education-India , 2nd edition, 2003, ISBN-13: 978-813170229

E-Resources:

1. <https://www.cs.princeton.edu/>
2. <https://www.opendatastructures.org/ods-cpp>
3. <https://www.lib.mdp.ac.in/ebook/DSa>
4. <https://ww.cs-fundamentals.com/data-structures/introduction-to-datastructures.php>
5. <https://www.cprogramming.com/algorithms-and-data-structures.html>
6. <https://www.cprograms.in/>

Electronic Devices

Course Code	L:T:P:S	Credits	Exam marks	Exam Duration	Course Type
18ECT353	4:0:0:0	4	CIE:50 SEE:50	3 hours	FE

Course Objectives:

This course will enable students to:

- Understand the principles for construction and operations of different MOSFET modes.
- Perform the AC and DC analysis of various MOSFET configurations.
- Understand the concept of small signal analysis on various types of FET's.
- Study the functionality of various two terminal semiconductor devices.
- Learn the working principle of different PNP devices and their applications.

Syllabus

Module – I

Field-Effect Transistors : Introduction, Construction and Characteristics of JFETs, Transfer Characteristics, Important Relationships, Depletion-Type MOSFET, Enhancement-Type MOSFET, CMOS. **10 Hours**

Module – II

FET Biasing : Introduction, Fixed-Bias Configuration, Self-Bias Configuration, Voltage-Divider Biasing, DC analysis of Depletion-Type MOSFETs and Enhancement-Type MOSFETs. **10 Hours**

Module – III

FET Small-Signal Analysis : Introduction, FET Small-Signal Model, JFET Fixed-Bias and Voltage divider bias Configuration, Depletion-Type MOSFETs and Enhancement-Type MOSFETs. **10 Hours**

Module – IV

Other Two-Terminal Devices: Introduction, Schottky Barrier (Hot-Carrier) Diodes, Varactor (Varicap) Diodes, Photodiodes, Photoconductive Cells, IR Emitters, Liquid-Crystal Displays, Solar Cells. **11 Hours**

Module – V

PNPN and Other Devices : Introduction, Silicon-Controlled Rectifier, Basic Silicon-Controlled Rectifier Operation, Unijunction Transistor, Photo transistors, Opto-Isolators, Programmable Uni-junction Transistor. **11 Hours**

Course Outcomes :

On completion of this course, students should be able to:

- Analyze the construction and working principles of JFETs, MOSFET and CMOS.
- Design the different methods of FET biasing.
- Design and implement small signal FET amplifier circuits.
- Examine the applications of different types of two terminal devices like Schottky Diode, Photo Diode.
- Illustrate the operations of PNP devices like SCR, UJT.

Text Book:

1. Robert L. Boylestad and Louis Nashelsky: "Electronic Devices and Circuit Theory", (Chapters 6-8,16,17), 10th Edition, PHI/Pearson Education, 2012, ISBN: 978-81-317-6459-6.

Reference Books:

1. Jacob Millman and Christos C. Halkias: "Integrated Electronics", 2nd Edition, Tata - McGraw Hill, 2010, ISBN-978-007-015142-0.
2. B. Mahadevaswamy: "Analog Electronics Circuits: A Simplified Approach", 1st Edition, Pearson/Saguine, 2010, ISBN: 978-81-317-3234-2.
3. David A. Bell: "Electronic Devices and Circuits", 5th Edition, PHI, 2008, ISBN: 978-0-19-569340-9.

E-Resources:

1. <http://www.circuitstoday.com/fet-biasing>
2. <http://www.mhhe.com/engcs/electrical/neamen01/ch06.pdf>
3. <https://www.elprocus.com/semiconductor-devices-types-and-applications/>



Analog Electronic Circuits Laboratory

Course Code	L:T:P:S	Credits	Exam marks	Exam Duration	Course Type
18ECL36	1:0:2:0	2	CIE:50 SEE:50	3 Hours	FC

Course Objectives:

This course will enable students to :

- Gain knowledge about basic operations of analog devices.
- Understand the applications of different semiconductor devices.
- Design different types of diode circuits.
- Construct different types of transistor circuits.
- Use appropriate test equipment to analyze circuit operations.

Syllabus

List of Experiments

- 1 Design of Clamping circuits: positive clamping / negative clamping.
- 2 Design and testing of Diode clipping (Single/Double ended) circuits for peak clipping, peak detection.
- 3 Design and testing of Half wave Rectifier with and without Capacitor filter. Determination of ripple factor and efficiency.
- 4 Design and testing of Full wave Rectifier with and without Capacitor filter. Determination of ripple factor and efficiency.
- 5 Design and testing of Bridge Rectifier with and without Capacitor filter. Determination of ripple factor and efficiency.
- 6 Design of RC coupled Single stage BJT amplifier and determination of the gainfrequency response.
- 7 Design of BJT Darlington Emitter follower and determination of the gain frequency response.
- 8 (Single circuit) (One Experiment).
- 9 Design and testing for the performance of BJT-RC Phase shift Oscillator for $f_0 \leq 10$ KHz.
- 10 Design and testing for the performance of BJT – Hartley and Colpitts Oscillators for RF range $f_0 \geq 50$ KHz.
- 11 Design and testing for the performance of BJT -Crystal Oscillator for $f_0 > 100$ KHz.
- 12 Design and Testing of a transformer less Class – B push pull power amplifier and determination of its conversion efficiency.

Course Outcomes:

On completion of this course, students will able to:

- Design various types of clipping and clamping circuits.
- Analyze and design different rectifiers.
- Design BJT amplifier and power amplifier.
- Design and evaluate Darlington emitter follower.
- Design and evaluate the performance of various types of oscillators.

E-Resources:

1. <http://www.allaboutcircuits.com/textbook/semiconductors/chpt-3/introduction-to-diodes-and-rectifiers/>
2. <http://fourier.eng.hmc.edu/e84/lectures/ch2/node4.html>
3. <http://www.allaboutcircuits.com/video-lectures/transistor-biasing/>



Career Skill Development Programme

Course Code	L:T:P:S	Credits	Exam marks	Exam Duration	Course Type
18CSD37	0:2:0:0	1	CIE:50 SEE:50	3 Hours	S&H

Course Objectives:

This course will enable students to :

- To implement English vocabulary at command and ensure language proficiency.
- To Achieve better Technical writing and Presentation skills Identify the common errors in speaking and writing English.
- Acquire Employment and Workplace communication skills.
- Language Lab.
- For augment LSRW and GV skills (Listening, Speaking, Reading, Writing and Grammar, Vocabulary) through tests, activities, exercises etc., comprehensive web-based learning and assessment systems can be referred.

Syllabus

Module – I

Introduction, Leaving taking, Request for Repetition, Asking for Information, Complaining, Asking about Preferences, Agreeing and Disagreeing, Starting a conversation with a stranger, Making Requests, Expressing Gratitude, Complimenting and Congratulating, Apologizing and Responding an Apology, Expressing Sympathy, Seeking Permission, Offering to Help, Ending a Conversation. **05 Hours**

Module – II

Oral Presentation, IT Extempore/Public speaking. **05 Hours**

Module – III

Story telling, debating. **06 Hours**

Module – IV

Mock interview, Telephonic Interview, Group Discussion, Personal Interview, Employment Interview, Dialogues in Various Situations. **05 Hours**

Module – V

Non-Verbal Communication skills (Body Language), Communication at Workplace Interpersonal Communication skills. **05 Hours**

Course Outcomes:

On completion of this course, students will be able to :

- Identify common errors in spoken and written communication
- Get familiarized with English vocabulary and language proficiency

- Improve nature and style of sensible writing and acquire employment and workplace communication skills
- Improve their Technical Communication Skills through Technical Reading and Writing practices
- Perform well in campus recruitment, engineering and all other general competitive examinations

Text Book:

1. S.L.N.SHARMA, K.SHANKARANARAYANA: “Basic Grammar”, Navakarnataka Vin-yasa Pvt. Ltd.,

Reference Books :

1. Communication Skills by Sanjay Kumar and PushpLata, Oxford University Press - 2018. Refer it’s workbook for activities and exercises — “Communication Skills — I (A Workbook)” published by Oxford University Press—2018.
2. English Language Communication Skills (Lab Manual cum Workbook), Cengage learning India Pvt Limited [Latest Revised Edition]—2018. Reference Books.
3. English for Technical Communication by N.P.Sudharshana and C.Savitha, Cambridge University Press— 2016.



Constitution of India, Professional Ethics and Human Rights

Course Code	L:T:P:S	Credits	Exam marks	Exam Duration	Course Type
18CPH38	1:0:0:0	1	CIE:50 SEE:50	2 hours	HSS

Course Objectives:

This course will enable students to :

- • The basic information about Indian constitution.
- • The fundamental rights and duties of a citizen.
- • Special privileges of socially and economically weaker sections of the society.
- • Individual role and ethical responsibility towards society.

Syllabus

Module – I

Introduction to the Constitution of India, The Making of the Constitution and Salient features of the Constitution. Preamble to the Indian Constitution, Fundamental Rights & its limitations. **03 Hours**

Module – II

Directive Principles of State Policy & Relevance of Directive Principles of State Policy, Fundamental Duties. Union Executives – President, Prime Minister, Parliament, Supreme Court of India. **03 Hours**

Module – III

State Executives – Governor, Chief Minister, State Legislature High Court of State. Electoral Process in India, Amendment Procedures, 42nd , 44th , 74th , 76th , 86th & 91st Amendments. **02 Hours**

Module – IV

Special Provision for SC & ST, Special Provision for Women, Children & Backward Classes, Emergency Provisions. Human Rights- Working of National Human Rights Commission in India, Powers and functions of Municipalities, Panchayats and Co - Operative Societies. **03 Hours**

Module – V

Scope & Aims of Engineering Ethics, Responsibility of Engineers, Impediments to Responsibility. Risks, Safety and liability of Engineers, Honesty, Integrity & Reliability in Engineering. **02 Hours**

Course Outcomes:

On completion of this course, students will be able to:

- Familiarize with fundamental rights and duties.
- Recognize the Electoral Process.
- Get exposed to legislature and judiciary.
- Realize special provisions given for women, children and weaker section of society.
- Exhibit Engineering ethics and responsibilities of Engineers.

Text Books:

1. Durga Das Basu, "Introduction to the Constitution of India", Lexis Nexis Publications; 22nd Edition, 2015, ISBN-13: 978-9351434467.
2. Charles E. Haries, Michael S Pritchard and Michael J. Robins, "Engineering Ethics", Thomson Wadsworth, 2nd Edition, 2003, ISBN-13: 978-9812436764.

Reference Books:

1. M.V. Pylee, "An Introduction to Constitution of India", Vikas Publishing, 2002, 1st Edition, ISBN-13: 978-8125918325.
2. M. Govindarajan, S. Natarajan, V.S. Senthilkumar, "Engineering Ethics", PHI Learning Private Limited, New Delhi, 2nd Edition, 2013, ISBN-13: 978-8120348165.
3. Brij Kishore Sharma, "Introduction to the Constitution of India", PHI Learning Private Limited, New Delhi, 7th Edition, 2015, ISBN-13: 978-8120350892.

E-Resources:

1. <http://www.cgsird.gov.in/constitution.pdf>
2. <http://indiacode.nic.in/coiweb/welcome.html>

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Fourier Series and Probability Theory (IC)

Course Code	L:T:P:S	Credits	Exam marks	Exam Duration	Course Type
18ECM41	3:0:2:0	4	CIE:50 SEE:50	3 Hours	FC

Course Objectives:

This course will enable the students to:

- Perform the basic calculations, array operations, vector operations, statistic and probability distribution using R
- Understand the trigonometric series and harmonic analysis.
- Analyze complex functions
- Understand the concept of different probability distributions.
- Analyze the concept of stochastic process and gain the knowledge of sampling of large data

Syllabus

Module – I

R LAB : Introduction to R, Basic Data types, vector operations, matrix construction, lists, data frames, Elementary statistics with R-Qualitative and Quantitative data, Numerical measures, probability distribution, interval estimation and simple linear regression **07 Hours**

Module – II

Fourier Series : Fourier Series of periodic functions, half range of sine and cosine series, complex Fourier Series, applications of Fourier Series , Harmonic analysis. **08 Hours**

Module – III

Complex Variables : Complex variables: Functions of a complex variable, derivative of complex functions. Analytic functions: Cauchy's-Riemann equations in Cartesian and polar forms (No problems by using limits), Harmonic functions, construction of analytic functions by using Milne-Thomson method. Cauchy Theorem, Cauchy's integral formula problems. **08 Hours**

Module – IV

Random Variables and Probability Distribution : Discrete and continuous Random Variables, Probability density function, binomial, Poisson's and normal distribution, joint Probability distribution with two Discrete random variables **08 Hours**

Module – V

Stochastic Process And Sampling Distribution : Probability vectors, stochastic matrices, fixed point matrices, regular , stochastic matrices, Markov's Chains , higher

transition probabilities, stationary distribution of regular Markov's Chains and absorbing states.

Sampling Distribution, Testing of hypothesis, level of significance, confidence limits, test of significance of large samples, sampling of variables, central limit theorem, confidence limits for unknown means, Students t-Distribution **08 Hours**

List of R-Lab Programs

1. Introduction to R software and basic commands.
2. Operation of Vectors.
3. Operations of matrices.
4. Demonstration of Data frames.
5. Qualitative Data Analysis.
6. Probability Distribution.

Course Outcomes:

On completion of this course the students are able to

- Understand the R software to solve various engineering problems.
- Express periodic function as infinite series.
- Determine the differentiation and integrals of complex functions.
- Calculate the probability distributions and solve real time problems.
- Analyze the large data using sampling techniques.

Text Books:

1. Dr. B.S. Grewal: "Higher Engineering Mathematics", (Chapters 10,20,26,27), Khanna Publishers, New Delhi, 42nd Edition, 2012, ISBN: 9788174091956.
2. N.P. Bali and Dr. Manish Goyal, "A Text Book of Engineering Mathematics", (Chapters : 10,19,21), Laxmi Publications (P) Ltd., New Delhi, 9th Edition, 2014, ISBN: 9788131808320.
3. W.N.Venables, D.M.Smith : "An introduction to R".

Reference Books:

1. Erwin Kreyszig: "Advanced Engineering Mathematics", (Chapters 11,13, 14,24,25), Wiley Pvt. Ltd., India, New Delhi, 9th Edition, 2011, ISBN 13: 9788126531356.
2. B.V. Ramana: "Higher Engineering Mathematics", (Chapters 17, 22, 23, 27, 28, 29, 31), Tata McGraw – Hill Publishing Company Limited, New Delhi, 2nd Reprint, 2010, ISBN 13: 978-0-07063419-0.

E-Resources:

1. <http://www.zums.ac.ir/ebooks/mathematics/essential-engineering-mathematic>.
2. <https://archive.org/details/AdvancedEngineeringMathematics10thEdition>
3. <https://www.r-project.org/>

Fundamentals of HDL

Course Code	L:T:P:S	Credits	Exam marks	Exam Duration	Course Type
18ECT42	4:0:0:0	4	CIE:50 SEE:50	3 Hours	FC

Course Objectives:

This course will enable students to :

- Understand the basic concept of HDL and to compare between Verilog and VHDL.
- Study the fundamentals of data flow and behavioral designs.
- Understand the concept of structural and mixed language descriptions.
- Apply the concept of task, functions and file processing in HDL.
- Apply the concept of map Register Transfer Logic code to hardware domain.

Syllabus

Module – I

Introduction: Why HDL? , A Brief History of HDL, Structure of HDL Module, Operators, Data types, Types of Descriptions, simulation and synthesis, brief comparison of VHDL and Verilog. **10 Hours**

Module – II

Data-Flow Descriptions: Highlights of Data-Flow Descriptions, Structure of Data flow Description, Data Type – Vectors.

Behavioral Descriptions: Behavioral Description highlights, Structure of HDL behavioral Description, The VHDL variable: Assignment Statement, Sequential statements. **11 Hours**

Module – III

Structural Description: Highlights of structural description, organization of structural description.

Mixed-Language Descriptions: Highlights of Mixed-Language Description, How to invoke one language from the other, Mixed Language Description examples, and Limitations of Mixed-Language Description. **11 Hours**

Module – IV

Procedures, Task and Functions: Highlights of Procedures, tasks, and Functions, Procedures and tasks, Functions. AdvancedHDLDescriptions: File Processing, Examples of File Processing. **10 Hours**

Module – V

Synthesis Basics: Highlights of Synthesis, Synthesis information from Entity and Module, Mapping Process and always in the Hardware Domain. **10 Hours**

Course Outcomes:

On completion of this course, the students are able to :

- Describe the various data type, operators and different descriptions in VHDL and Verilog.
- Develop program using data flow and behavioral descriptions in VHDL and Verilog.
- Develop program using structural and mixed language description in VHDL and Verilog.
- Develop program using procedure, task, and functions in VHDL and Verilog.
- Analyze and synthesis VHDL and VERILOG codes for digital circuits.

Text Book:

1. Nazeih M. Botros: “HDL Programming (VHDL and Verilog)”, (Chapters 1-5), Dreamtech Press Publishers, New Delhi, 2008, ISBN-13: 9788177226973.

Reference Books:

1. J. Bhaskar: “A Verilog HDL Primer”, 2nd Edition, BS Publications, Hyderabad, 2001, ISBN: 8178000121.
2. Volnei A. Pedroni: ‘Circuit Design with VHDL’, 1st Edition, Prentice Hall of India Pvt. Ltd., New Delhi, 2004, ISBN: 8120326830.

E-Resources:

1. <http://www.xilinx.com/video/hardware/basic-hdl-coding-techniques.html>
2. http://www.academia.edu/1492361/VHDL_BASIC_WITH_EXAMPLES
3. <http://ece-research.unm.edu/jimp/vlsi/slides/vhdl.html>
4. <http://www.asic-world.com/systemverilog/basic1.html>
5. http://www.referencedesigner.com/tutorials/verilog/verilog_01.php



Signals and Systems (IC)

Course Code	L:T:P:S	Credits	Exam marks	Exam Duration	Course Type
18ECI43	3:0:2:0	4	CIE:50 SEE:50	3 Hours	FC

Course Objectives:

This course will enable students to :

- Understand the basic types of continuous-time and discrete-time signals and system properties.
- Study the convolution operation on continuous and discrete LTI system.
- Apply the concept of differential and difference equations in LTI systems and express the system in block diagram representations.
- Apply the various properties of continuous and discrete Fourier transform for the given signals.
- Explain the properties of Z-Transforms and role of ROC for evaluating causality, stability of the given signal.

Syllabus

Module – I

Introduction to signals and systems: Classification of continuous and discrete time signals, basic operations on independent variables, elementary signals: Exponential, sinusoidal signals, unit impulse, unit step and unit ramp signals and their properties, systems viewed as interconnection of operations, properties of systems. **07 Hours**

Module – II

Time-domain representations for LTI systems : convolution sum, Difference equation representation of an LTI System, Solutions for Difference equation, Block diagram representation-direct form-I and direct form- II difference equations. **08 Hours**

Module – III

Fourier Transform : Continuous Fourier transforms and Discrete Fourier transform and their properties. Application of Fourier transform-solving difference and differential equation, frequency response, impulse response, Comment on stability and causality analysis. **08 Hours**

Module – IV

Z-Transforms-1 : Introduction, definition of Z-transform, properties of ROC. Properties of Z transform, inverse Z-transforms using partial fraction. **08 Hours**

Module – V

Z-Transforms-2 : Transform analysis of LTI Systems using Z-transform, system function, causality, stability, Unilateral Z-Transform and its application to solve difference Equation. **08 Hours**

List of Experiments

1. Plotting the continuous time and Discrete time signals
2. Basic operations performed on signals.
3. Linear Convolution of given two sequences.
4. Impulse response of a given system.
5. Circular convolution of given two sequences.
6. Solving a given difference equation.

Course Outcomes:

On completion of this course, the students are able to :

- Discriminate various elementary signals and identify the properties of systems.
- Compute convolution operation on discrete time signals and express difference equations as block diagram.
- Express the signals using Fourier transform and apply their properties for solving differential and difference equation.
- Analyze Z transforms and inverse Z transforms using various methods.
- Analyze LTI systems using unilateral Z transforms.

Text Books:

1. Simon Haykin: "Signals and Systems", 4th Edition, John Wiley India Pvt. Ltd, Re-print, 2004, ISBN: 978-81-265-1265-2.
2. Michael J Roberts: "Fundamentals of Signals and Systems", 2nd Edition, Tata Mc Graw-Hill, 2010, ISBN: 978-0-07-070221-9.

Reference Books:

1. Alan V Oppenheim, Alan S, Willsky and Hamid Nawab: "Signals and Systems", 2nd Edition, Pearson Education Asia / PHI, Indian Reprint, 2002, ISBN: 81-203-1246-5.
2. H.P Hsu, R. Ranjan, "Signals and Systems", Scham's Outlines, TMH, 1995, ISBN-13: 978-0-07-060171-0.

E-Resources:

1. http://link.springer.com/chapter/10.1007/978-1-4020-6272-8_4#page-1
2. <http://www.thefouriertransform.com/>
3. <http://ipsa.swarthmore.edu/LaplaceZTable/LaplaceZFuncTable.html>



Linear IC's and Applications(IC)

Course Code	L:T:P:S	Credits	Exam marks	Exam Duration	Course Type
18ECI441	3:0:2:0	4	CIE:50 SEE:50	3 hours	FE

Course Objectives:

This course will enable students to :

- Understand the use of op amp in DC and AC applications.
- Explain the concepts of practical OP-AMP specifications, characteristics, biasing of OP-AMPs.
- Describe the frequency response and bandwidth performance of practical OP-AMP.
- Describe the opamp as current amplifier, peak detectors, wave form generators.
- Understand the concept of 555 timer, PLL and its applications.

Syllabus

Module – I

Operational Amplifier Fundamentals : Basic OP-AMP circuit, OP-AMP parameters: Input and Output voltage, CMRR and PSRR, Offset voltages and currents, Input and Output impedances, Slew rate and Frequency limitations.

OP-AMPs as DC Amplifiers : Biasing OP-AMPs, Direct coupled -Voltage Followers, Non-inverting Amplifiers, Inverting amplifiers, Summing amplifiers, Difference amplifier. **08 Hours**

Module – II

OP-AMPs as AC Amplifiers : Capacitor coupled Voltage Follower, High input impedance-Capacitor coupled Voltage Follower, Capacitor coupled Non-inverting Amplifiers, High input impedance - Capacitor coupled Non-inverting Amplifiers, Capacitor coupled Inverting amplifiers, setting the upper cut-off frequency, Capacitor coupled Difference amplifier, Use of a single polarity power supply. **08 Hours**

Module – III

OP-AMPs frequency response and compensation: Circuit stability, Frequency and phase Response, Frequency compensating methods, Band width, Slew rate effects, Zin Mod compensation, and circuit stability precautions. **07 Hours**

Module – IV

OP-AMP Application : Current amplifiers, precision rectifiers, Clamping circuits, Peak detectors, sample and hold circuits, Log and antilog amplifiers, Multiplier and divider, Triangular / rectangular wave generators.

Non-linear circuit applications of OP-AMP: Crossing detectors, inverting Schmitt trigger circuits, Mono-stable and A-stable multivibrator, Active Filters: First and Second order Low pass and High pass filters. **08 Hours**

Module – V

Other Linear IC applications: 555 timer: Basic timer circuit, 555 timer used as a stable and Mono-stable multivibrator, Schmitt trigger; PLL: Operating principles, Phase detector / Comparator, VC; D/A and A/ D converters: Basic DAC Techniques, AD converters, IC 723 general purpose Regulator. **08 Hours**

List of Experiments

1. Design a Second order active LPF
2. Design a Second order active HPF
3. Design a Second order active BPF
4. Design and test a Schmitt trigger circuit for the given values of UTP and LTP
5. Design and test R-2R DAC using op-amp
6. Design and test the circuits Astablemultivibrator for given frequency and duty cycle using IC 555
7. Design and test the circuits Monostablemultivibrator for given pulse width W using IC555

Course Outcomes:

On completion of this course, the students are able to :

- Describe the practical OP-AMP specifications and characteristics.
- Determine OP-AMP as AC amplifiers.
- Analyzing stability condition of OP-AMP.
- Analyzing OP-AMP linear applications like Crossing detectors, inverting Schmitt trigger circuits, Mono-stable and A-stable multivibrator, Active Filters.
- Analyzing of 555 timers as a stable and Mono-stable multivibrator, Schmitt trigger, PLL and their applications.

Text Books:

1. David A. Bell: “Operational Amplifiers and Linear IC’s”, 2nd Edition, (Chapters 1-4), PHI/Pearson, 2008, ISBN: 9788120323599.
2. D. Roy Choudhury and Shail B. Jain: “Linear Integrated Circuits”, 4th Edition, (Chapter 5), New Age International, 2010, ISBN: 9788122430981.

Reference Books:

1. Robert F. Coughlin and Fred F. Driscoll: “Operational Amplifiers and Linear Integrated Circuits”, 6th Edition, PHI/Pearson, 2001, ISBN: 8120320964.
2. Ramakant A. Gayakwad: “OP-AMPS and Linear Integrated Circuits”, 4th Edition, PHI/Pearson, 2000, ISBN: 8120320581.

E-Resources:

1. http://www.electronics-tutorials.ws/opamp/opamp_1.html
2. http://www.radio-electronics.com/info/circuits/opamp_basics/operational-amplifier-basics-tutorial.php
3. http://www.chem.uoa.gr/applets/appletopamps/appl_opamps2.html
4. <http://www.allaboutcircuits.com/textbook/semiconductors/chpt-8/introduction-operational-amplifiers/>



Transmission Lines and Waveguides (IC)

Course Code	L:T:P:S	Credits	Exam marks	Exam Duration	Course Type
18ECI442	3:0:2:0	4	CIE:50 SEE:50	3 hours	FE

Course Objectives:

This course will enable students to :

- Understand the concepts of Transmission lines and its applications using Smith chart.
- Describe rectangular waveguides and its modes of operation.
- Analyze Directional Couplers and have a basic idea about circulators and isolators.
- Understand the concept of Strip lines and Microstrip lines
- Analyze the origins of RADAR and its applications.

Syllabus

Module – I

Microwave Transmission Lines : Introduction, Transmission Lines, Equations and Solutions, Reflection and Transmission Coefficients, Standing Waves and SWR, Smith Chart, Impedance matching using Single Stubs, Microwave coaxial connectors and Adapters.

Microwave Waveguides and Components : Rectangular waveguides, Circular Waveguides, Directional Couplers, Circulators and Isolators. **08 Hours**

Module – II

Microwave Diodes : Transfer electron devices: Introduction, GUNN effect diodes- GaAs diode, RWH theory, Modes of Operation, Avalanche transit time devices: READ diode, IMPATT diode, parametric amplifiers. **08 Hours**

Module – III

Microwave Network Theory and Passive Devices: Symmetrical Z and Y parameters for reciprocal networks, S matrix representation of Multi port networks, Phase shifters, Attenuators, Waveguide Tees, Magic Tees. **08 Hours**

Module – IV

Strip Lines: Introduction, Micro strip lines, Parallel strip lines, coplanar strip lines, shielded strip lines. **07 Hours**

Module – V

An Introduction to Radar: Basic Radar, The simple form of the Radar equation, Radar block diagram, radar frequencies, applications and origins of Radar. MTI and Pulse Doppler Radar: Introduction to Doppler and MTI Radar. **08 Hours**

List of Experiments

1. To conduct an experiment to obtain the mode curves of Reflex Klystron.
2. To conduct an experiment to find power division and isolation of a Magic Tee.
3. Measurement of Frequency, Wavelength, Power, VSWR and Attenuation in a microwave test bench using Klystron.
4. Determination of Coupling and Isolation characteristics of a Stripline-Directional Coupler.
5. To conduct an experiment to determine the V-I curve of a GUNN diode, measurement of a guide wavelength (λ_g), frequency and VSWR.
6. Measure of power division and isolation characteristics of a microstrip 3 dB power divider.

Course Outcomes:

On completion of this course, the students are able to :

- Define line parameters and analyze various transmission lines and resonators.
- Explain the basic concepts of diodes and its applications.
- Apply the concepts of S parameters to analyze waveguide Tees.
- Analyze the differences between various strip lines and its applications
- Apply the concepts of RADAR to find range and velocity of the target object.

Text Books:

1. Microwave Devices and Circuits- Samuel Y Liao, 3rd edition, Pearson, 2011.
2. Introduction to Radar systems-Merill I Skolnik
3. Microwave Engineering- Annapurna Das, Sisir K Das, TMH Publication, 2nd Edition, 2010.

Reference Books:

1. "Microwave Engineering"- David M Pozar, 3rd edition, Wiley, 2011..

E- Reference

1. <https://www.maximintegrated.com/en/app-notes/index.mvp/id/742>
2. <https://www.elprocus.com/gunn-diode-working-characteristics-and-its-applications/>
3. <http://www.radartutorial.eu/11.coherent/co13.en.html>
4. <http://www.rfwireless-world.com/Articles/types-and-basics-of-microstrip-line.html>
5. <https://www.microwaves101.com/encyclopedias/magic-tees>
6. <http://www.radio-electronics.com/info/antennas/waveguide/waveguide-basics-tutorial.php>

Electronic Instrumentation (IC)

Course Code	L:T:P:S	Credits	Exam marks	Exam Duration	Course Type
18ECI443	3:0:2:0	4	CIE:50 SEE:50	3 hours	FE

Course Objectives:

This course will enable students to:

- Understand the concepts of errors and characteristics of the measurement systems.
- Understand the basic working principle of DVM, DMM, CRO, DFM.
- Study the fundamental concepts and working of CRT.
- Describe the functioning of different Signal Generators and Bridge Circuits.
- Understand the principle of Different types of Transducers.

Syllabus

Module – I

Introduction:

(a) Measurement Errors: Gross errors and systematic errors, Absolute and relative errors, accuracy, Precision, Resolution and Significant figures.

(b) Voltmeters and Multimeters Introduction, Multirange voltmeter, Extending voltmeter ranges, Loading, Pea responding and True RMS voltmeters. **07 Hours**

Module – II

Digital Instruments : Digital Voltmeters – Introduction, DVM's based on $V - T$, $V - F$ and Successive approximation principles, Resolution and sensitivity, General specifications, Digital Multi-meters, Digital frequency meters, Digital measurement of time. **08 Hours**

Module – III

Oscilloscopes : Introduction, Basic principles, CRT features, Block diagram and working of each block, Typical CRT connections, Dual beam and dual trace CROs, Electronic switch. **08 Hours**

Module – IV

(a)Signal Generators : Introduction, Fixed and variable AF oscillator, Standard signal generator, Laboratory type signal generator, AF sine and Square wave generator, Function generator, Square and Pulse generator, Sweep frequency generator, Frequency synthesizer.

(b)Measurement of resistance, inductance and capacitance: AC bridges, Capacitance Comparison Bridge, Maxwell's bridge, Wein's bridge, Wagner's earth connection. **08 Hours**

Module – V

Transducers - I : Introduction, Electrical transducers, Selecting a transducer, Resistive transducer, Resistive position transducer, Strain gauges, Resistance thermometer, Thermistor, Inductive transducer, Differential output transducers and LVDT.

Transducers - II : Piezoelectric transducer, photoelectric transducer, Photovoltaic transducer, Semiconductor photo devices, Temperature transducers-RTD, Thermocouple.

08 Hours

List of Experiments

1. Measurement of displacement using LVDT.
2. Measurement of load using Strain Gauge based load cell.
3. Measurement of temperature by RTD.
4. Study of P, PI and PID controllers.
5. Study of storage oscilloscope and determination of Transient response of RLC circuit.
6. Design and test a signal conditioning circuit for any transducer.

Course Outcomes :

On completion of this course, students will be able to:

- Analyze characteristics of various measuring instruments and different types of errors.
- Describe the various technologies available to measure R, L, C using current and voltage meters.
- Describe working principle of CRO and able to measure different parameters.
- Explain different types of Signal generators and function generator.
- Describe the working of different types of Transducers.

Text Books:

1. H. S. Kalsi: "Electronic Instrumentation", 3rd Edition, TMH, New Delhi, 2010, ISBN: 978-0-07-070206-6.
2. David A Bell: "Electronic Instrumentation and Measurements", 3rd Edition, PHI, New Delhi, 2006, ISBN: 9788120323605.

Reference Books:

1. Cooper D and A D Helfrick: “Modern electronic instrumentation and measuring techniques”, PHI, New Delhi, 1990, ISBN: 978-81-203-0752-0.

E-Resources:

1. <http://www.testandmeasurementtips.com/oscilloscopes/different-types-of-oscilloscopes>
2. [http://www.myclassroom.com/Engineering-branches/21/Electronics-andInstrumentation-Engg.- \(EIE\)](http://www.myclassroom.com/Engineering-branches/21/Electronics-andInstrumentation-Engg.-(EIE))
3. http://www.radio-electronics.com/info/t_and_m/generators/signal-generator-types.php
4. <http://www.delabs-circuits.com/ciridir/analog/analog2.html>



Renewable Energy Resources

Course Code	L:T:P:S	Credits	Exam marks	Exam Duration	Course Type
18EET451	4:0:0:0	4	CIE:50 SEE:50	3 Hours	EE

Course Objectives:

This course will enable students to :

- Provide detailed information of the present energy scenario and the available Renewable Energy Resources.
- Get detailed insight knowledge in basics of solar radiation geometry and various measurement techniques.
- Understand the solar energy through solar thermal devices, PV conversion and their performance analysis.
- Gain the conceptual knowledge about the various energy conversion methods such as Wind, Tidal, OTEC and Geothermal.
- Give introduction to energy from Biomass, Hydrogen energy and their impact on environment and sustain ability.

Syllabus

Module – I

Introduction: Energy source, India's production and reserves of commercial energy sources, need for non-conventional energy sources.

Solar Radiation: Extra-Terrestrial radiation, spectral distribution of extraterrestrial radiation, solar constant, solar radiation at the earth's surface, beam, diffuse and global radiation, solar radiation data.

Measurement of Solar Radiation: Pyrometer, shading ring pyrheliometer, sunshine recorder, schematic diagrams and principle of working. **10 Hours**

Module – II

Solar Radiation Geometry : Flux on a plane surface, latitude, declination angle, surface azimuth angle, hour angle, zenith angle, solar altitude angle expression for the angle between the incident beam and the normal to a plane surface (No derivation), and local apparent time. Apparent motion of sun, day length, numerical examples.

Radiation Flux on a Tilted Surface: Beam, diffuse and reflected radiation, expression For flux on a tilted surface (no derivations), numerical examples.

Solar Thermal Conversion: Collection and storage, thermal collection devices, liquid flat plate collectors, solar air heaters concentrating collectors (cylindrical, parabolic, paraboloid) (Quantitative analysis). **10 Hours**

Module – III

Performance Analysis of Liquid Flat Plate Collectors : General description, collector geometry, selective surface (qualitative discussion) basic energy-balance equation, stagnation temperature, transmissivity of the cover system, transmissivity-absorptivity product, numerical examples. The overall loss coefficient, correlation for the top loss coefficient, bottom and side loss coefficient, problems (all correlations to be provided). Temperature distribution between the collector tubes, collector heat removal factor, collector efficiency factor and collector flow factor, mean plate temperature, instantaneous efficiency (all expressions to be provided). Effect of various parameters on the collector performance; collector orientation, selective surface, fluid inlet temperature, number covers, dust. **12 Hours**

Module – IV

Photovoltaic Conversion : Description, principle of working and characteristics, applications.

Wind Energy: Properties of wind, availability of wind energy in India, wind velocity and power from wind; major problems associated with wind power, Wind machines: Types of wind machines and their characteristics, horizontal and vertical axis wind mills.

Tidal Power: Tides and waves as energy suppliers and their mechanics, fundamental characteristics of tidal power, harnessing tidal energy, limitations.

Ocean Thermal Energy Conversion: Principle of working, Rankin cycle.

Geothermal Energy Conversion: Principle of working, Types of geothermal station With schematic diagram. **10 Hours**

Module – V

Energy from Bio Mass: Photosynthesis, photosynthetic oxygen production, energy plantation, bio gas production from organic wastes by anaerobic fermentation, description of bio-gas plants, transportation of bio-gas, problems involved with bio-gas production, application of bio-gas, application of bio-gas in engines, advantages.

Hydrogen Energy : Properties of Hydrogen with respected to its utilization as a renewable form of energy, sources of hydrogen, production of hydrogen, electrolysis of water, thermal decomposition of water, thermo chemical production bio-chemical production. **10 Hours**

Course Outcomes:

On completion of the course, the students will be able to,

- Explain the present energy scenario and the available Renewable Energy Resources.

- Describe the basics of solar radiation geometry and various measurement techniques.
- Analyze the knowledge gained in tapping the solar energy through solar thermal devices, pv conversion and their performance analysis.
- Demonstrate the various energy conversion methods such as Wind, Tidal, OTEC and Geothermal.
- Apply knowledge of Biomass and Hydrogen energy and their impact on environment and sustainability.

Text Books:

1. G D Rai: “Non-Conventional Energy Sources”, (Chapters 1-3,6-9,11), 5th Edition, Khanna Publishers, 2011, ISBN-13: 9788174090737.
2. John Twidell and Tony Weir: “Renewable Energy Resources”, (Chapters 2,5-7,9-14), 3rd Edition, Routledge Publisher, 2015, ISBN-13: 978041558437.
3. N K Bansal: “Non-Conventional Energy Resources”, (Chapters 1-3,9,10,12,13), 1st Edition, Vikas Publishing, 2014, ISBN-13: 978935978577.

Reference Books:

1. B H Khan: “Non-Conventional Energy Resources”, (Chapters 4-10), 2nd Edition, Tata McGraw-Hill Pub., 2006, ISBN-13: 9780070142763.
2. S P Sukhatme, J K Nayak, “Solar Energy”, (Chapters 3,4), 3rd Edition, Tata McGraw-Hill Pub., 2008, ISBN-13: 9780070260641.



INTRODUCTION TO CYBER SECURITY AND CYBER LAWS

Course Code	L:T:P:S	Credits	Exam marks	Exam Duration	Course Type
18EET452	04:0:0:0	04	CIE:50 SEE:50	3 Hours	EE

Course Objectives:

This course will enable students to :

- Understand the area of cyber security.
- Know and understand the technological aspects of Information Security.
- Understand various cyber threats.
- Understand and recognize the cyber forensics.
- Know Legal Perspectives in cyber security.

Syllabus

Module - I

Introduction to Cyber crime and Information Security : Cyber crime: Definition and Origins of the Word, Who are Cyber criminals? Classifications of Cyber crimes, Introduction to information security, Need for Information security, Threats to Information Systems. Cyber Offenses: How Criminals Plan Them: How Criminals Plan the Attacks, Social Engineering, Cyber stalking, Cyber cafe and Cyber crimes, Botnets: The Fuel for Cyber crime, Attack Vector, Cloud Computing. **11 Hours**

Module – II

Tools and Methods Used in Cyber crime : Introduction, Proxy Servers , Key loggers and Spy wares, Back doors, Steganography, SQL Injection, Buffer Overflow, Attacks on Wireless Networks. Phishing and Identity Theft: Introduction, Phishing, Identity Theft (ID Theft). Security Technology - Firewall and VPNs, Intrusion Detection, Access Control. **10 Hours**

Module – III

Cyber Threats : -Viruses, Worms, Trojan Horse, Bombs, Trapdoors, Spoofs, E-mail viruses, Macro viruses, Malicious Software, Network and Denial of Services Attack, DDOS, Security Threats to E-Commerce- Electronic Payment System, e- Cash, Credit/Debit Cards. Digital Signature, Public Key Cryptography. **10 Hours**

Module – IV

Understanding Computer Forensics : Digital Forensics Science, The Need for Computer Forensics, Forensics Analysis of E-Mail, Digital Forensics Life Cycle, Chain of Custody Concept, Network Forensics, Approaching a Computer Forensics Investigation, Setting up a Computer Forensics Laboratory: Understanding the Requirements, Relevance of the OSI 7 Layer Model to Computer Forensics, Challenges in Computer Forensics,

Module – V

Security Policies : Security Policies, Why Policies should be developed, WWW policies, Email Security policies, Policy Review Process-Corporate policies-Sample Security Policies, Publishing and Notification Requirement of the Policies. Information Security Standards-ISO, IT Act, Copyright Act, Patent Law, IPR. Cyber Laws in India; IT Act 2000 Provisions, Intellectual Property Law: Copy Right Law, Software License.

10 Hours

Course Outcomes:

On successful completion of the course, the students will be able to:

- Understand the basics of cyber security.
- Explain the functionalities of cyber/information security.
- Be aware of various cyber threats.
- Understanding cyber forensics.
- Describe IT Act, Suggest appropriate security countermeasures for the given scenario.

Text Books:

1. Sunit Belapure and Nina Godbole, “Cyber Security: Understanding Cyber Crimes, Computer Forensics And Legal Perspectives”, Wiley India Pvt Ltd, ISBN: 978-81-265-21791, Publish Date 2013
2. V.K. Pachghare, “Cryptography and information Security”, PHI Learning Private Limited, Delhi India.

Reference Books :

1. Dr. Surya Prakash Tripathi, Ritendra Goyal, Praveen kumar Shukla ,”Introduction to Information Security and Cyber Law” Willey Dreamtech Press.
2. Thomas J. Mowbray, “Cybersecurity: Managing Systems, Conducting Testing, and Investigating Intrusions”, Copyright © 2014 by John Wiley & Sons, Inc, ISBN: 978-1-118 - 84965 -1.

E-Resources :

1. <https://slideplayer.com/slide/12803493/>



MANAGEMENT INFORMATION SYSTEM

Course Code	L:T:P:S	Credits	Exam marks	Exam Duration	Course Type
18EET453	4:0:0:0	4	CIE:50 SEE:50	3 Hours	EE

Course Objectives:

This course will enable students to :

- Effectively use and administrate information systems in different business applications.
- Understand problem solving techniques to model information system solutions for business problems.
- Understand the usage of intranet and extranets in management information systems.
- Analyze the principles of Transaction Processing Systems.
- Understand the business and professional responsibilities related to the use of information system in Organizations.

Syllabus

Module – I

Foundations of information systems in business: Introduction to Information Systems in Business: Why study Information Systems?, What you need to know, A global Information society, Success and Failure with IT, Why Businesses need Information Technology. Fundamentals of Information Systems: Fundamental Information System concepts: System concepts, Components of an Information System, Information System Resources, Information System activities, Overview of Information Systems: The expanding Role of Information Systems, Operations support Systems, Management support Systems, Other classifications of Information Systems. **10 Hours**

Module – II

Solving Business Problems with Information Systems: A Systems Approach to problem Solving: The Systems approach, Defining problems and opportunities, Developing alternative solutions, Evaluating Alternative solutions, Selecting the best solution, Using the Systems approach. Developing Information System Solutions: The system development cycle, Starting the Systems Development process, Systems Analysis, Systems Design, Prototyping, Implementing a new Information System, Maintenance of Information System, Computer Aided Systems Engineering, End user development. Business applications –I The Internet, Electronic Commerce and Business: Introduction, Business use of the Internet, Interactive marketing, Business value of the Internet, Customer value and the Internet. Fundamentals of Electronic

Commerce: Introduction, Foundations and applications of e-commerce, Business to Consumer and Business to Business commerce, Electronic payments and security.

10 Hours

Module – III

Intranets, Extranets, and Enterprise Collaboration: Intranets and Extranets in Business: Business Value, Applications and Technologies for Intranets, Role of Extranets, Enterprise Collaboration Systems: Enterprise Collaboration, Group Ware, Electronic communication and Conferencing tools, collaborative work management tools. Information Systems for Business Operations: Business Information Systems: Cross Functional Marketing, Manufacturing, Human Resources, Accounting and Financial Information Systems. **10 Hours**

Module – IV

Transaction Processing Systems: Transaction Processing, Data entry, Batch and Real-time processing, Database maintenance, Document and Report generation, Inquiry processing.

Business applications -II

Information Systems for Strategic Advantage: Introduction, Competitive strategy, Strategic Roles for Information System, Breaking Business Barriers, Value chain and strategic Information System, Strategic Applications and Issues in information Technology, Re-engineering Business process, Improving Business quality, Becoming an agile competitor. Creating a virtual Company, Building the knowledge-creating company, Using the Internet Strategically. **11 Hours**

Module – V

Managing information technology: Enterprise and global Management: Managing Information Resources and Technologies: Information Technology Architecture, Managers and Information Technology, Organizations and Information Technology, Information Resource Management, Strategic Management Operational Management, Resource Management, Technology Management, Global Information Technology Management: The International Dimension, Global IT Management, Cultural, Political and Geo-Economic challenges, The global company, Global Business and IT strategies, Global Business and IT applications, Global IT Platforms, Global data Issue, Global Systems development, You and Global IT Management, Planning. Implementing change: Planning for Business change with IT: Organizational planning, Information System planning Methodologies, The scenario approach, planning for competitive advantage, Critical success factors, Business Systems Planning, Computer Aided Planning tools, implementing business change. **11 Hours**

Course Outcomes:

On completion of this course, students will able to:

- Describe the roles and functionalities of information system.
- Analyze types of solutions for business and its applications.
- Analyze the usage of Intranet and Extranet in business applications.
- Describe database management and competitive strategic approach of information systems in business applications.
- Describe various approaches in managing information technology.

Text Books:

1. James O'brien, George Marakas: "Management Information System", 10th Edition, McGraw Hill Education, 2010, ISBN-13: 978-0-07-337681-3, ISBN: 0-07337681-7.
2. M V Gandhi, B S Thomson: "Smart Materials and Structures", (Chapters 13-75), 1st Edition, Chapman and Hall Pub., 1992, ISBN-13: 9780412370106.

Reference Books:

1. Kenneth C. Laudon and Jane P. Laudon: "Management Information System, Managing the Digital Firm", 11th Edition, Pearson Education, 2006.
2. Steven Alter: "Information Systems-The Foundation of E-Business", 4th Edition, Pearson Education, 2002.

E-Resources:

1. https://books.google.co.in/books/about/Management_Information_System.html.
2. <http://www.pearsoned.co.uk/bookshop>



Environmental Air Pollution

Course Code	L:T:P:S	Credits	Exam marks	Exam Duration	Course Type
18EET454	4:0:0:0	4	CIE:50 SEE:50	3 Hours	EE

Course Objectives:

- The fundamentals of air pollution with a background on historical perspective on air pollution and current air quality policies and standards.
- Major air pollutants, their sources and their effects (environmental, economic and health) and how emissions are estimated from road traffic and industrial sources.
- Dispersion of air pollutants in the atmosphere.
- To analyze and present outputs of air quality models to a wide range of audiences.
- Different air quality monitoring equipments.

Syllabus

Module - I

Introduction: Definition – Classification and Characterization of Air Pollutants, Emission Sources, Behaviour and Fate of air pollutants, Chemical Reactions in the Atmosphere, Photo-chemical Smog, Coal-induced Smog.

Effects of Air Pollution: On Human Health, Animals, Plants and Materials – Major Environmental Air Pollution Episodes–London Smog, Los Angeles Smog and Bhopal Gas Tragedy. **08 Hours**

Module - II

Meteorology: Introduction – Meteorological Variables, Primary and Secondary Lapse Rate, Inversions, Stability Conditions, Windrose, General Characteristics of Stack Plumes, Meteorological Models. Factors to be considered in Industrial Plant Location and Planning, Noise pollution sources, measurement units, effects and control. **10 Hours**

Module - III

Sampling, Analysis and Control: Sampling and Measurement of Gaseous and Particulate matter, Stack Sampling, Analysis of Air Pollutants, Smoke and Smoke Measurement.

Air Pollution Control Methods: Particulate, Emission Control, Gravitational Settling Chambers, Cyclone Separators, Fabric Filters, Electrostatic Precipitators, Wet Scrubbers, Selection of a Particulate Collecting Equipment. **10 Hours**

Module - IV

Control of Gaseous Emissions, Adsorption by Liquids, Adsorption by Solids,

Combustion Odors and their control.

Air Pollution due to Automobiles: Air Pollution due to Gasoline Driven and Diesel Driven Engines, Effects, Direct and Indirect Methods of control. **10 Hours**

Module - V

Burning Environmental Issues: Acid Rain, Global Warming, Ozone Depletion in Stratosphere, Indoor Air Pollution.

Environmental Legislation: Environmental Policy, Environmental Acts, Water, Air and Noise Pollution Standards. **10 Hours**

Course Outcomes:

On completion of this course, the students are able to :

- Examine emission standards for industrial and other sources.
- Identify air pollution concentrations as a function of emission, meteorology, topography and the built environment.
- Discuss impact of air pollution on health of humans, animals, plants and materials.
- Identify different equipments for air quality monitoring and control.
- Distinguish between global and local effects of air pollution as well to recognize the legal aspects associated with air pollution.

Text Books:

1. Rao M.N. and Rao H.V.N: "Air Pollution", (Chapters 1-6), Tata- McGraw- Hill Publishing Company Ltd., New Delhi, India, 2011, ISBN 13: 978-0074518717.
2. Anjaneyulu Y: "Air Pollution and control Technologies", (Chapters 2-5), Allied Publishers, Delhi, 2012, ISBN- 13: 9788177641844.
3. Rao C.S: "Environmental Pollution Control Engineering", (Chapters 2-6), New age International Publishers, New Delhi, 2nd Edition, 2013, ISBN- 13: 978-8122418354.

References Books:

1. Gilbert M Masters: "Introduction To Environmental Engineering and Science", (Chapters 1-4), Pearson Education, 3rd Edition, 2007, ISBN-13: 978-0131481930.
2. Mahajan.S.P: "Pollution Control in Process Industries", (Chapters 1-5), Tata McGraw Hill Publishing Co., New Delhi, 2010, ISBN-13: 978-0074517727.

E-Resources:

1. <http://nptel.ac.in/courses/105102089/>
2. <http://nptel.ac.in/courses/105104099/>
3. nptel.ac.in/courses/103107084/module1/lecture1/lecture1.pdf



Fundamentals of HDL Lab

Course Code	L:T:P:S	Credits	Exam marks	Exam Duration	Course Type
18ECL46	1:0:2:0	2	CIE:50 SEE:50	3 Hours	FC

Course Objectives:

This course will enable students to understand :

- The concept of HDL programming for Logic gates.
- The Hardware description programs for combinational and sequential circuits.
- The design of synchronous and asynchronous counters.
- The different types of interfacing components.
- The interfacing of DC , stepper motors.

Syllabus

List of Experiments

1. Write HDL code to realize all the logic gates.
2. Write a HDL program for the following combinational designs,
 - i. Decoder.
 - ii. Encoder.
 - iii. Multiplexer and de-multiplexer.
 - iv. Comparator.
3. Write a HDL code to describe the functions of a Full Adder Using three modeling styles.
4. Develop the HDL code for the following Flip-Flops, SR, D, JK, T.
5. Design 4 bit binary, BCD counters (Synchronous reset and Asynchronous reset).

Interfacing:

1. Write HDL code to control speed and direction of Stepper motor.
2. Write HDL code to generate different waveforms (Square, Triangle, Ramp etc.,) using DAC Change the Frequency and amplitude.
3. Write HDL code to control speed of DC motor.

Course Outcomes :

On completion of this course, students will able to:

- Develop HDL programs for Logic gates.
- Develop HDL programs for combinational designs.
- Develop HDL programs for sequential designs.
- Develop HDL programs for various counters.
- Analyze and Interface with various electrical components.

E- Resources:

1. <http://www.xilinx.com/video/hardware/basic-hdl-coding-techniques.html>
2. http://www.academia.edu/1492361/VHDL_BASIC_WITH_EXAMPLES
3. <http://ece-research.unm.edu/jimp/vlsi/slides/vhdl.html>
4. <http://www.asic-world.com/systemverilog/basic1.html>
5. http://www.referencedesigner.com/tutorials/verilog/verilog_01.php
6. <http://vhdlguru.blogspot.in/p/example-codes.html>



Technical Report Writing & Irdp

Course Code	L:T:P:S	Credits	Exam marks	Exam Duration	Course Type
I8CSD47	0:2:0:0	1	CIE:50 SEE:50	3 Hours	S&H

Course Objectives:

This course will enable students to :

- To implement English vocabulary at command and ensure language proficiency
- To Achieve better Technical writing and Presentation skills Identify the common errors in speaking and writing English
- Acquire Employment and Workplace communication skills
- Language Lab
- For augment LSRW and GV skills (Listening, Speaking, Reading, Writing and Grammar, Vocabulary) through tests, activities, exercises etc., comprehensive web-based learning and assessment systems can be referred.

Syllabus

Module – I

An Introduction, Specimen Summary, Narrative Composition, Descriptive composition, Argumentative composition. **05 Hours**

Module – II

Formal Letter writing, Personal letter writing, E-mail writing, Notice writing. **05 Hours**

Module – III

Report writing, Resume writing, Comprehension skills. **06 Hours**

Module – IV

Summary writing, Essay writing, Paragraph writing, Critical Writing, Factual Writing. **05 Hours**

Module – V

Comprehension-Analysis with suggested Answers, Comprehension -Sense and Interpretation, Correction of Errors. **05 Hour**

Course Outcomes :

On completion of this course, students will be able to:

- Identify common errors in spoken and written communication
- Get familiarized with English vocabulary and language proficiency
- Improve nature and style of sensible writing and acquire employment and workplace communication skills

- Improve their Technical Communication Skills through Technical Reading and Writing practices
- Perform well in campus recruitment, engineering and all other general competitive examinations

Text Book:

1. S.L.N.SHARMA, K.SHANKARANARAYANA: “Basic Grammar”, Navakarnataka Vin-yasa Pvt. Ltd.,

Reference Books:

1. Communication Skills by Sanjay Kumar and PushpLata, Oxford University Press - 2018. Refer it’s workbook for activities and exercises — “Communication Skills — I (A Workbook)” published by Oxford University Press—2018.
2. English Language Communication Skills (Lab Manual cum Workbook), Cengage learning India Pvt Limited [Latest Revised Edition]—2018. Reference Books.
3. English for Technical Communication by N.P.Sudharshana and C.Savitha, Cambridge University Press— 2016.



Vyavaharika Kannada

Course Code	L:T:P:S	Credits	Exam marks	Exam Duration	Course Type
18KAK38 / 18KVK38	1:0:0:0	1	CIE:50 SEE:50	1 Hours	S&H

Course Objectives:

- The course will enable the students to understand Kannada and communicate in Kannada language.

Syllabus

Module - 1

Vyavaharika kannada – Parichaya (Introduction to Vyavaharika Kannada).

Module - 2

Kannada Aksharamale haagu uchcharane (Kannada Alfabets and Pronunciation).

Module - 3

Sambhashanegaagi Kannada Padagalu (Kannada Vocabulary for Communication).

Module - 4

Kannada Grammar in Conversations (Sambhashaneyalli Kannada Vyakarana).

Module - 5

Activities in Kannada.

Course Outcomes:

- At the end of the course, the student will be able to understand Kannada and communicate in Kannada language.



Aadalitha Kannada

Course Code	L:T:P:S	Credits	Exam marks	Exam Duration	Course Type
18KAK38 / 18KVK38	1:0:0:0	1	CIE:50 SEE:50	1 Hours	S&H

ಆಡಳಿತ ಕನ್ನಡ ಕಲಿಕೆಯ ಉದ್ದೇಶಗಳು

- ಪದವಿ ವಿದ್ಯಾರ್ಥಿಗಳಾಗಿರುವುದರಿಂದ ಆಡಳಿತ ಕನ್ನಡದ ಪರಿಚಯ ಮಾಡಿಕೊಡುವುದು.
- ವಿದ್ಯಾರ್ಥಿಗಳಲ್ಲಿ ಕನ್ನಡ ಭಾಷೆಯ ವ್ಯಾಕರಣದ ಬಗ್ಗೆ ಅರಿವು ಮೂಡಿಸುವುದು.
- ಕನ್ನಡ ಭಾಷಾ ರಚನೆಯಲ್ಲಿನ ನಿಯಮಗಳನ್ನು ಪರಿಚಯಿಸುವುದು.
- ಕನ್ನಡ ಭಾಷಾ ಬರಹದಲ್ಲಿ ಕಂಡುಬರುವ ದೋಷಗಳು ಹಾಗೂ ಅವುಗಳ ನಿವಾರಣೆ ಮತ್ತು ಲೇಖನ ಚಿಹ್ನೆಗಳನ್ನು ಪರಿಚಯಿಸುವುದು.
- ಸಾಮಾನ್ಯ ಅರ್ಜಿಗಳು, ಸರ್ಕಾರಿ ಮತ್ತು ಅರೆ ಸರ್ಕಾರಿ ಪತ್ರ ವ್ಯವಹಾರದ ಬಗ್ಗೆ ಅರಿವು ಮೂಡಿಸುವುದು.
- ಭಾಷಾಂತರ ಮತ್ತು ಪ್ರಬಂಧ ರಚನೆ ಬಗ್ಗೆ ಅಸಕ್ತಿ ಮೂಡಿಸುವುದು.
- ಕನ್ನಡ ಭಾಷಾಭ್ಯಾಸ ಮತ್ತು ಸಾಮಾನ್ಯ ಕನ್ನಡ ಹಾಗೂ ಆಡಳಿತ ಕನ್ನಡದ ಪದಗಳ ಪರಿಚಯ ಮಾಡಿಕೊಡುವುದು.

ಪರಿವಿಡಿ (ಪಠ್ಯಪುಸ್ತಕದಲ್ಲಿರುವ ವಿಷಯಗಳ ಪಟ್ಟಿ)

ಅಧ್ಯಾಯ - 1

ಕನ್ನಡಭಾಷೆ - ಸಂಕ್ಷಿಪ್ತ ವಿವರಣೆ.

ಅಧ್ಯಾಯ - 2

ಭಾಷಾ ಪ್ರಯೋಗದಲ್ಲಾಗುವ ಲೋಪದೋಷಗಳು ಮತ್ತು ಅವುಗಳ ನಿವಾರಣೆ.

ಅಧ್ಯಾಯ - 3

ಲೇಖನ ಚಿಹ್ನೆಗಳು ಮತ್ತು ಅವುಗಳ ಉಪಯೋಗ.

ಅಧ್ಯಾಯ - 4

ಪತ್ರ ವ್ಯವಹಾರ.

ಅಧ್ಯಾಯ - 5

ಆಡಳಿತ ಪತ್ರಗಳು.

ಅಧ್ಯಾಯ - 6

ಸರ್ಕಾರದ ಆದೇಶ ಪತ್ರಗಳು.

ಅಧ್ಯಾಯ - 7

ಸಂಕ್ಷಿಪ್ತ ಪ್ರಬಂಧ ರಚನೆ (ಪ್ರಿಸೈಸ್ ರೈಟಿಂಗ್), ಪ್ರಬಂಧ ಮತ್ತು ಭಾಷಾಂತರ.

ಅಧ್ಯಾಯ - 8

ಕನ್ನಡ ಶಬ್ದಸಂಗ್ರಹ.

ಅಧ್ಯಾಯ - 9

ಕಂಪ್ಯೂಟರ್ ಹಾಗೂ ಮಾಹಿತಿ ತಂತ್ರಜ್ಞಾನ.

ಅಧ್ಯಾಯ - 10

ಪಾರಿಭಾಷಿಕ ಆಡಳಿತ ಕನ್ನಡ ಪದಗಳು ಮತ್ತು ತಾಂತ್ರಿಕ / ಕಂಪ್ಯೂಟರ್ ಪಾರಿಭಾಷಿಕ ಪದಗಳು.

ಆಡಳಿತ ಕನ್ನಡ ಕಲಿಕೆಯ ಫಲಿತಾಂಶಗಳು

- ಆಡಳಿತ ಭಾಷೆ ಕನ್ನಡದ ಪರಿಚಯವಾಗುತ್ತದೆ.
- ವಿದ್ಯಾರ್ಥಿಗಳಲ್ಲಿ ಕನ್ನಡ ಭಾಷೆಯ ವ್ಯಾಕರಣದ ಬಗ್ಗೆ ಅರಿವು ಮೂಡುತ್ತದೆ.
- ಕನ್ನಡ ಭಾಷಾ ರಚನೆಯಲ್ಲಿನ ನಿಯಮಗಳು ಮತ್ತು ಲೇಖನ ಚಿಹ್ನೆಗಳು ಪರಿಚಯಿಸಲ್ಪಡುತ್ತವೆ.
- ಸಾಮಾನ್ಯ ಅರ್ಜಿಗಳು, ಸರ್ಕಾರಿ ಮತ್ತು ಅರೆ ಸರ್ಕಾರಿ ಪತ್ರವ್ಯವಹಾರದ ಬಗ್ಗೆ ಅರಿವು ಮೂಡುತ್ತದೆ.
- ಭಾಷಾಂತರ ಮತ್ತು ಪ್ರಬಂಧ ರಚನೆ ಬಗ್ಗೆ ಅಸಕ್ತಿ ಮೂಡುತ್ತದೆ.
- ಕನ್ನಡ ಭಾಷಾಭ್ಯಾಸ ಮತ್ತು ಸಾಮಾನ್ಯ ಕನ್ನಡ ಹಾಗೂ ಆಡಳಿತ ಕನ್ನಡದ ಪದಗಳು ಪರಿಚಯಿಸಲ್ಪಡುತ್ತವೆ.

ಪರೀಕ್ಷೆಯ ವಿಧಾನ :

1. ನಿರಂತರ ಆಂತರಿಕ ಮೌಲ್ಯಮಾಪನ – CIE (Continuous Internal Evaluation):
2. ಕಾಲೇಜು ಮಟ್ಟದಲ್ಲಿಯೇ ಆಂತರಿಕ ಪರೀಕ್ಷೆಯನ್ನು 100 ಅಂಕಗಳಿಗೆ ವಿಶ್ವವಿದ್ಯಾಲಯದ ನಿಯಮಗಳು ಮತ್ತು ನಿರ್ದೇಶನದಂತೆ ನಡೆಸತಕ್ಕದ್ದು.

ಪಠ್ಯಪುಸ್ತಕ :

1. ಆಡಳಿತ ಕನ್ನಡ ಪಠ್ಯ ಪುಸ್ತಕ (Kannada for Administration), ಸಂಪಾದಕರು : ಡಾ.ಎಲ್. ತಿಮ್ಮೇಶ, ಪ್ರೊ. ವಿ. ಕೇಶವಮೂರ್ತಿ,

ಪ್ರಕಟಣೆ :

1. ಪ್ರಸಾರಾಂಗ, ವಿಶ್ವೇಶ್ವರಯ್ಯ ತಾಂತ್ರಿಕ ವಿಶ್ವವಿದ್ಯಾಲಯ, ಬೆಳಗಾವಿ.

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